

# FURUNO

## SERVICE MANUAL

**MARINE VHF  
HANDHELD TRANSCEIVER**

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**MODEL FM-55**

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**FURUNO ELECTRIC CO., LTD.**  
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## SPECIFICATIONS OF MARINE VHF TRANSCEIVER FM-55

GENERAL

- |                         |   |
|-------------------------|---|
| 1. Frequency Range      | TX: 155.050 to 163.900 MHz<br>RX: 155.050 to 163.900 MHz  |
| 2. Class of Emission    | Frequency modulation with a pre-emphasis of 6dB/octave (phase modulation)   |
| 3. Number of Channels   | 1) All channels for ITU<br>2) U.S. and Canadian Weather, 10ch<br>3) Up to 74 Private channels (factory-programmed)<br>* USA channels are available for U.S. market                                |
| 4. Channel Spacing      | 25 kHz  |
| 5. Communication System | Simplex and semi-duplex   |
| 6. Antenna              | Helical whip<br>(BNC connector provided for external 50-ohm antenna)  |
| 7. Power Supply         | Ni-Cd battery pack<br>(BP-507, BP-807, BP-509 or BP-512)  |
| 8. Ambient Temperature  | -20°C to +55°C  |
| 9. Dimensions           | 65(W) x 170(H) x 44(D)mm, 600g (with Battery Pack BP-507, BP-807 or BP-509)<br>65(W) x 190(H) x 44(D)mm, 650g (with Battery Pack BP-512)<br>* Dimensions do not include projections and controls. |

TRANSMITTER

- |                           |   |
|---------------------------|---|
| 1. RF Output Power        | 5, 3, 2 or 1W, subject to authority approval<br>Reducible to below 1W for 5-2W version, and about 0.5W for 1W version |
| 2. Frequency Stability    | <u>+1.5</u> kHz (-15°C to +55°C)  |
| 3. Frequency Deviation    | <u>+5</u> kHz max.  |
| 4. Modulation AF Response | Modulation index within +1 or -3 dB relative to its value at 1000 Hz for modulation frequencies 300 - 3000 Hz         |
| 5. Spurious Emissions     | Less than 2.5uW   |

## RECEIVER

- |                                 |  |
|---------------------------------|--|
| 1. Receiving System             | PLL synthesizer, Double superheterodyne                                      |
| 2. AF Output Power              | Approx. 250/500mW into 8 ohm speaker (see table below.)                      |
| 3. AF Response                  | 6 dB/oct de-emphasis +1, -3 dB for 300 ~ 3000 Hz                             |
| 4. Sensitivity                  | SINAD 12 dB: 0.32 uV (-10 dBu)<br>20 dB: 0.50 uV (-6 dBu)                    |
| 5. Adjacent Channel Selectivity | 70 dB (+15 to +35°C, nominal supply)<br>60 dB (-20 to +55°C, 90/130% supply) |
| 6. Spurious Response            | 70 dB (apart more than 25 kHz)   |
| 7. Intermodulation              | 70 dB  |
| 8. Squelch                      | Threshold 0.1 uV. Tight 0.2 uV   |

## BATTERY PACK (Ni-Cd type)

Type	BP-507	BP-509	BP-807	BP-509	BP-512
Capacity	450mAH		850mAH	450mAH	450mAH
No. of cells	6	8	6	8	10
Voltage	7.2V	9.6V	7.2V	9.6V	12V
TX output	1W/0.5W		2W/1W	3W/1W	5W/1W
AF output	250mW		250mW	500mW	500mW
Power consump.	TX	0.85A or less	0.85A or less	1.1A or less	1.3A or less
	RX	150mA or less	150mA or less	200mA or less	200mA or less
	ST-BY	less than 200mA			
Operating time	4 hours		4 hours	2 hours	2 hours
Battery life	More than 300 charges/discharges (Duty cycle TX:RX:ST-BY = 1:1:8 )				
Charging hours	For 450mAH type Battery Pack, approx. 15 hours with Wall Charger (BC-500) and 1 to 1.5 hours with Tabletop Quick Charger (BP-501, BP-503 or BP-506)				

## EQUIPMENT LIST

(Standard)

1. Main Unit
- 2 Ni-Cd Battery Pack BP-507, BP-807, BP-509 or BP-512
- 3 Wall Charger BC-500 for 110/220VAC
- 4 Helical Whip Antenna
- 5 Earphone
- 6 Vinyl Case
- 7 Hand Strap
- 8 Belt Clip

(Optional)

- 1 Tabletop Quick Charger BC-501 for one radio or BC-503 for 3 radios
- 2 External Speaker/Microphone
- 3 Hard Case (Holster type)
- 4 Shoulder Belt

## FREQUENCY/CHANNEL LIST

MARINE VHF CHANNEL FREQUENCIES (INTERNATIONAL VERSION)				MARINE VHF CHANNEL FREQUENCIES (USA VERSION)			
Channel	Ship Transmit	Ship Receive	Type of Operation	Channel	Ship Transmit	Ship Receive	Type of Operation
01	156.050	160.650	Public Correspondence, Port Operation	01	156.050	156.050	Port Operation, Commercial
02	156.100	160.700	Public Correspondence, Port Operation	02	156.100	156.100	
03	156.150	160.750	Public Correspondence, Port Operation	03	156.150	156.150	
04	156.200	160.800	Public Correspondence, Port Operation	04	156.200	156.200	
05	156.250	160.850	Public Correspondence, Port Operation	05	156.250	156.250	
06	156.300	156.300	Safety	06	156.300	156.300	Intership Safety
07	156.350	156.950	Public Correspondence, Port Operation	07	156.350	156.350	Commercial
08	156.400	156.400	Intership	08	156.400	156.400	Commercial (Intership)
09	156.450	156.450	Port Operation	09	156.450	156.450	Commercial and Non-commercial
10	156.500	156.500	Port Operation	10	156.500	156.500	Commercial
11	156.550	156.550	Port Operation	11	156.550	156.550	Commercial
12	156.600	156.600	Port Operation	12	156.600	156.600	Port Operation
13	156.650	156.650	Bridge-to-bridge, Navigational	13	156.650	156.650	Bridge-to-bridge, Navigational, 1W
14	156.700	156.700	Port Operation	14	156.700	156.700	Port Operation
15	156.750	156.750	Coast-to-ship, 1W	15		156.750	Environmental (Receive Only)
16	156.800	156.800	Distress, Safety and Calling	16	156.800	156.800	Distress, Safety and Calling
17	156.850	156.850	State-controlled, Ship-to-coast, 1W	17	156.850	156.850	State-controlled, Ship-to-coast, 1W
18	156.900	161.500	Port Operation	18	156.900	156.900	Commercial
19	156.950	161.550	Port Operation	19	156.950	156.950	Commercial
20	157.000	161.600	Port Operation	20	157.000	161.600	Port Operation
21	157.050	161.650	Port Operation	21	157.050	157.050	US Government
22	157.100	161.700	Port Operation	22	157.100	157.100	Coast Guard Liaison
23	157.150	161.750	Public Correspondence	23	157.150	157.150	US Government
24	157.200	161.800	Public Correspondence	24	157.200	161.800	Public Correspondence
25	157.250	161.850	Public Correspondence	25	157.250	161.850	Public Correspondence
26	157.300	161.900	Public Correspondence	26	157.300	161.900	Public Correspondence
27	157.350	161.950	Public Correspondence	27	157.350	161.950	Public Correspondence
28	157.400	162.000	Public Correspondence	28	157.400	162.000	Public Correspondence
60	156.025	160.625	Public Correspondence, Port Operation	60	156.025	160.625	
61	156.075	160.675	Public Correspondence, Port Operation	61	156.075	160.675	
62	156.125	160.725	Public Correspondence, Port Operation	62	156.125	160.725	
63	156.175	160.775	Public Correspondence, Port Operation	63	156.175	156.175	Vessel Traffic Service (USCG)
64	156.225	160.825	Public Correspondence, Port Operation	64	156.225	160.825	
65	156.275	160.875	Public Correspondence, Port Operation	65	156.275	156.275	Port Operation
66	156.325	160.925	Public Correspondence, Port Operation	66	156.325	156.325	Port Operation
67	156.375	156.375	Port Operation	67	156.375	156.375	Commercial (Intership)
68	156.425	156.425	Port Operation	68	156.425	156.425	Non-commercial
69	156.475	156.475	Port Operation	69	156.475	156.475	Non-commercial
71	156.575	156.575	Intership, Port Operation	71	156.575	156.575	Non-commercial
72	156.625	156.625	Intership	72	156.625	156.625	Non-commercial (Intership)
73	156.675	156.675	Port Operation	73	156.675	156.675	Port Operation
74	156.725	156.725	Port Operation	74	156.725	156.725	Port Operation
77	156.875	156.875	Intership, 1W	77	156.875	156.875	Port Operation (Intership), 1W
78	156.925	161.525	Port Operation	78	156.925	156.925	Non-commercial
79	156.975	161.575	Port Operation	79	156.975	156.975	Commercial
80	157.025	161.625	Port Operation	80	157.025	157.025	Commercial
81	157.075	161.675	Port Operation	81	157.075	157.075	US Government
82	157.125	161.725	Port Operation, Public Correspondence	82	157.125	157.125	US Government
83	157.175	161.775	Public Correspondence	83	157.175	157.175	US Government
84	157.225	161.825	Port Operation, Public Correspondence	84	157.225	161.825	Public Correspondence
85	157.275	161.875	Public Correspondence	85	157.275	161.875	Public Correspondence
86	157.325	161.925	Public Correspondence	86	157.325	161.925	Public Correspondence
87	157.375	161.975	Public Correspondence	87	157.375	161.975	Public Correspondence
88	157.425	162.025	Public Correspondence	88	157.425	157.425	Commercial (Intership)
				WX1		162.550	Weather (Receive Only)
				WX2		162.400	Weather (Receive Only)
				WX3		162.475	Weather (Receive Only)
				WX4		162.425	Weather (Receive Only)
				WX5		162.450	Weather (Receive Only)
				WX6		162.500	Weather (Receive Only)
				WX7		162.525	Weather (Receive Only)
				WX8		161.650	Weather (Receive Only)
				WX9		161.775	Weather (Receive Only)
				WX0		163.275	Weather (Receive Only)

## PRIVATE CHANNELS (Version 1.0)

CH	FREQUENCY			CH	FREQUENCY		
	TX	RX			TX	RX	
00	156.000	156.000		A0	155.050	155.050	
29	157.450	162.050		A1	155.500	155.500	
30	157.500	162.100		A2	155.525	155.525	
31	157.550	162.150		A3	155.575	155.575	
32	157.600	162.200		A4	156.750	156.750	
33	157.650	162.250		A5	157.300	157.300	
34	157.700	162.300		A6	157.400	157.400	
35	157.750	162.350		A7	157.500	157.500	
36	157.800	162.400		A8	157.600	157.600	
37	157.850	162.450	*1	A9	157.700	157.700	
37	157.850	157.850	*2	B0	157.800	157.800	
38	157.900	162.500		B1	159.300	159.300	
39	157.950	162.550		B2	160.300	160.300	
40	158.000	162.600		B3	160.975	160.975	
41	158.050	162.650		B4	161.000	161.000	
42	158.100	162.700		B5	161.025	161.025	
43	158.150	162.750		B6	161.050	161.050	
44	158.200	162.800		B7	161.075	161.075	
45	158.250	162.850		B8	161.100	161.100	
46	158.300	162.900		B9	161.375	161.375	
47	158.350	162.950		C0	161.400	161.400	
48	158.400	163.000		C1	161.425	161.425	
49	158.450	158.450		C2	161.450	161.450	
50	158.500	158.500		C3	161.475	161.475	
51	155.625	155.625		C4	162.575	162.575	
52	155.650	155.650		C5	163.900	163.900	*1
53	155.675	155.675		C5	155.850	155.850	*2
54	155.700	155.700					
55	155.725	155.725					
56	155.750	155.750					
57	155.775	155.775					
58	155.800	155.800					
59	155.825	155.825					
70	156.525	156.525					
75	156.775	156.775					
76	156.825	156.825					
89	157.475	162.075					
90	157.525	162.125					
91	157.575	162.175					
92	157.625	162.225					
93	157.675	162.275					
94	157.725	162.325					
95	157.775	162.375					
96	157.825	162.425					
97	157.875	162.475					
98	157.925	162.525					
99	157.975	162.575					

\*1: For USA, UK, CANADA and CHINA

\*2: Countries other than \*1



## CHAPTER 1. CIRCUIT DESCRIPTION

The FM-55 handheld transceiver consists of an RF board, AF board, CPU board, keyboard and LCD display. See the block diagram on page D-1.

### 1-1. Receiver

#### RF Stage

The receiver is a double superheterodyne system with intermediate frequencies of 16.9MHz and 455kHz.

An incoming signal is fed to the RF amplifier Q206 through the low-pass filter and antenna switching circuit. The low-pass filter, composed of C243 to C247 and L206 to L208 works only for transmitter. The output of Q206 is applied to the band-pass filter consisting of triple resonant circuits (L212 to L214 and C255 to C259).

In the first mixer Q207, the received signal is converted into 16.9MHz 1st IF signal. The local frequency injected to the 1st mixer, ranging from 156.05 to 163MHz depending on the channel selected, is generated from the voltage controlled oscillator U201. The resultant 1st IF signal is fed to complex FM IF module U207 via ceramic filter FL201.

#### FM IF Amplifier

All the basic functions to convert the 1st IF signal into an audio signal are included in this FM IF module; local oscillator, mixer, IF amp, discriminator, filter amplifier and squelch detector. The FM IF module converts the 16.9MHz IF signal into 455kHz 2nd IF signal by mixing the output from the 2nd local oscillator (16.445MHz). The discriminator demodulates the 2nd IF signal into an audio signal of 300Hz to 3kHz.

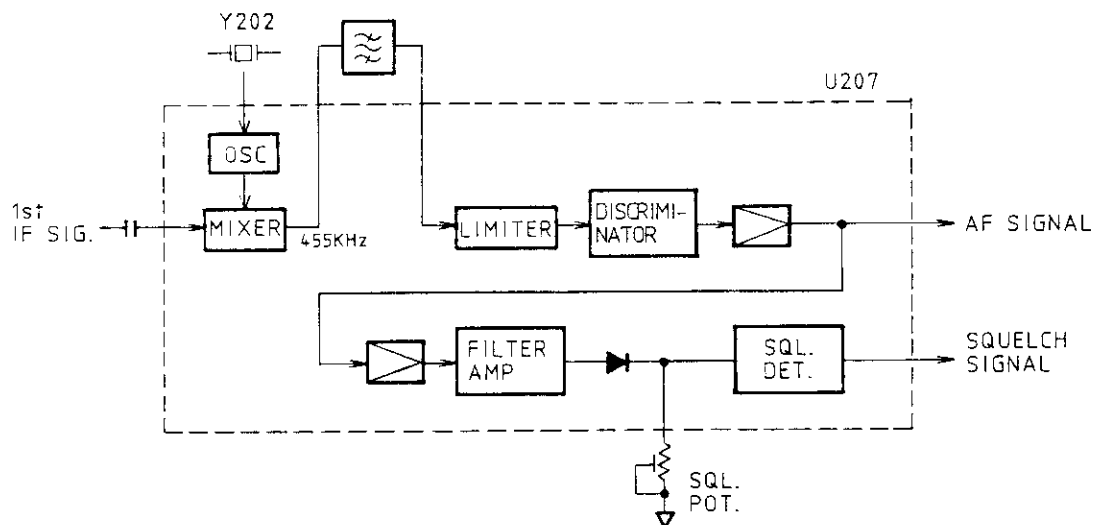


Fig.1-1 FM IF amp.

## Squelch/AF amp

The discriminator output is also fed to the filter amplifier where only the noise component is taken out. After being rectified and smoothed, the extracted noise is compared with the bias voltage set by the squelch potentiometer in the squelch detector. If the noise level is high, the squelch detector produces the "squelch signal" to mute the audio output. This squelch signal is also sent to the CPU and used for judging the presence or absence of an incoming signal in the dual watch operation.

Buffer amplifier Q502 on the AF board prohibits noise from U207 to go thru the audio amplifier U505 when the squelch signal is detected, and delivers the beep sound generated by the CPU to the AF amplifier.

U503 logic (2) detects operation of the PTT switch, and controls the DC switching circuits U507 and U506. U504 logic (1), in combination with U503 logic (2), controls transmission-reception switching functions, beep generation, squelch performance and TX muting.

Audio amplifier U505 delivers nominal 300/500mW audio signal to an 8 ohm built-in loudspeaker.

## 1-2. Transmitter

The voice signal from the microphone is pre-emphasized in the mic amplifier U501 and is delivered, via limiter CR501 and low-pass filter U502, to the VCO circuit for frequency modulation.

During reception, +5V power supplied to the above mentioned circuit is switched off to isolate the VCO from the mic amplifier, which may otherwise cause unwanted modulation of the RX 1st local frequency.

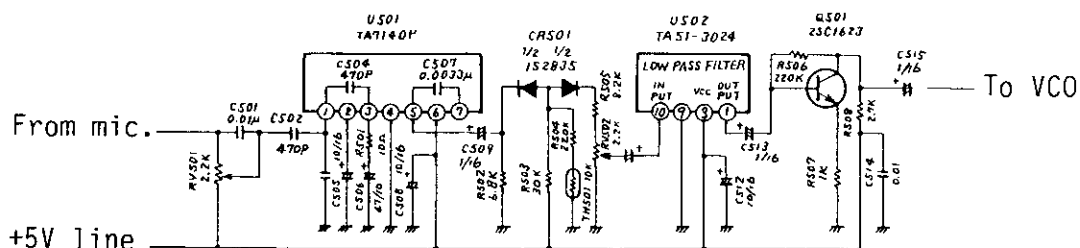


Fig.1-2

## PLL

A desired channel frequency is generated by the PLL synthesizer consisting of the 12.8MHz reference oscillator, phase comparator, VCO, prescaler and programmable counter as shown on Fig.1-3.

U203 is composed of a phase comparator, frequency divider, shift register and programmable counter. The frequency divider counts down the reference oscillator frequency of 12.8MHz to 25kHz which establishes the channel spacing of this transceiver.

When the phase-lock loop is in the state of "lock", VCO U201 produces channel frequency of 25kHz times N in crystal accuracy, where N is an integer the programmable counter is set to be divided. The CPU in the microprocessor recognizes the channel number when the keyboard is operated, and accesses the ROM (Read-Only-Memory) to take out the corresponding channel data "N" among channel information stored in it.

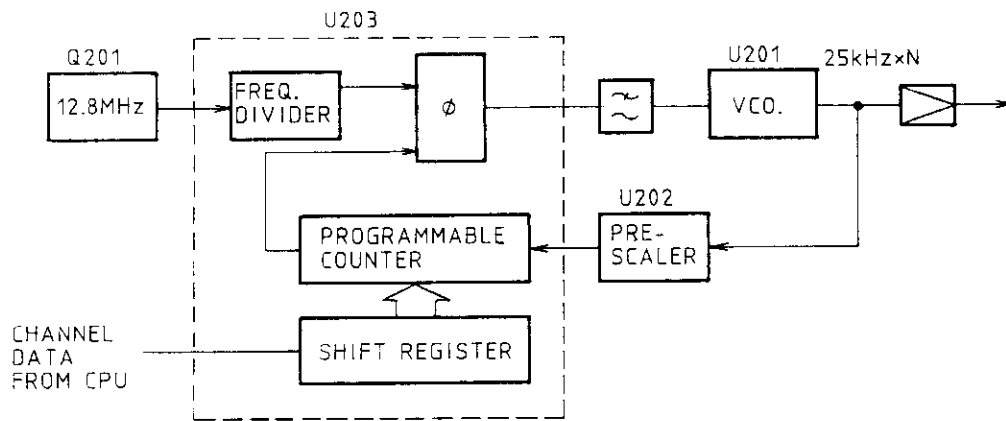


Fig.1-3 PLL

The shift register in U203 receives this channel data from the CPU in serial format and converts it into parallel data in order to preset the programmable counter.

When an invalid channel (29 thru 59, 89 thru 99, etc) is selected, it is automatically disabled by internal memory U1. Invalid channels cause the display to blink and the transmitter and the receiver to become inoperative. After blinking for 2 seconds, the equipment is set to a previous valid channel.

TX/RF Output

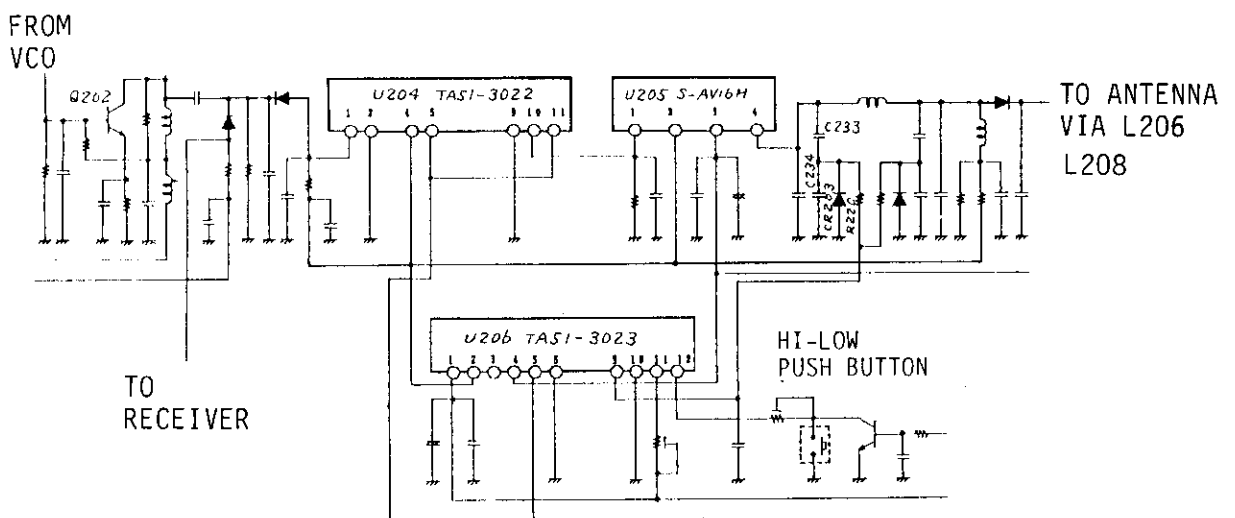


Fig.1-4 TX/RF output

An established channel frequency is taken from the VCO through the buffer Q202 to the diode switching circuit, which transfers the VCO output to either the transmitter driver stage or the receiver mixer circuit.

TX/RF amplifier U204 contains two stages of driver transistors of which the line voltage is controlled by the voltage shift action of power controller U206; the voltage at pin number 5 of U206 varies with the selection of the HI-LOW push button, resulting in switch over of the output power.

To provide stable output power irrespective of voltage deviation of the battery and to avoid abnormal emission, a negative feedback loop is employed in the TX power stage. The loop consists of C226, C227, CR203, R215 and U206, and controls the line voltage of U204 in the same way as the output power setting.

The band-pass filter consisting of L206 thru L208 filters out frequency components higher than 156.05 thru 163 MHz to minimize spurious emission.

### 1-3. System Controller

Microprocessor U1 comprises a 4-bit CPU, ROM(4096 x 8 bits), RAM(224 x 4 bits), 8-bit parallel serial interface, and LCD controller/driver. The Read-Only Memory (ROM) stores instructions for intended operation associated with the keyboard entry. Weather channels are also stored in the ROM for the USA version. Random-Access Memory (RAM) controls memory write-read operation in maximum 10 memory addresses. The CPU inhibits transmission on specific channels according to the ITU or relevant authorities' requirements, and controls high-low output selection, PLL synthesizer, squelch level, beep generation, etc.

### 1-4. Low Voltage Detector

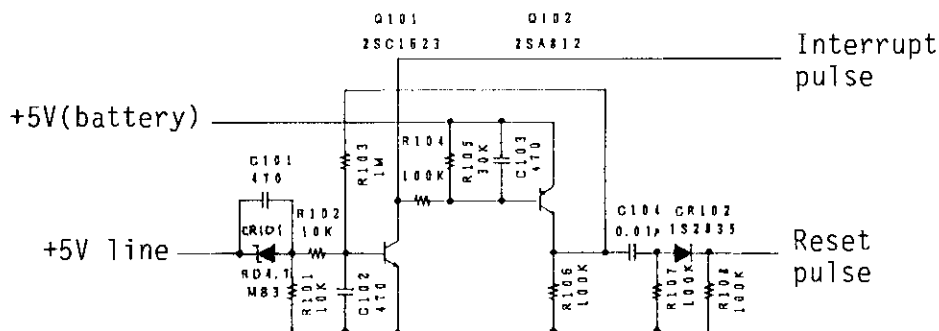


Fig.1-4 Low voltage detector

Q101 and Q102 form a low-voltage detector. The detector generates an interrupt pulse when the +5V line voltage drops below 4.7V and locks

the CPU to preserve the status of the present keyboard settings, otherwise the CPU will operate abnormally, causing loss of memory contents. On the other hand, the reset pulse triggers the CPU at the moment of power-on to recall the previous settings.

## CHAPTER 2. ADJUSTMENT

This chapter describes the basic technical data required for checking or alignment of the equipment. Because most parts are made up of high integar modules and employ thick-film technology, thereby reducing physical size and providing stable performance, only a few adjustments are required, unlike the conventional type of VHF transceiver

To gain access to the potentiometers inside the unit for adjustment, see page 2-3, "Disassembling the Unit".

## 2-1. Necessary Test Instrument

test instrument	minimum requirement	used for
Multimeter		Voltage check
RF Power Meter	Dummy terminated impedance: 50 ohms Capacity: 5W Freq. range: 200MHz	Power check
VHF Freq. Counter	Freq. range: DC - 200MHz	Freq. check
Directional Coupler & Termination	Impedance: 50 ohms	
Linear detector (Deviation meter)	Freq range: 140 - 170MHz Deviation range: 3 - 10 kHz	Deviation check
Oscilloscope	Freq. response: DC - 200MHz Sensitivity: 10mV/div.	
VHF/FM Sig. Generator	Freq range: 200MHz max. Output level: -15 to +110dBu Output impedance: 50 ohms	Sensitivity check
Distortion Meter		

## 2-2. Precaution on Adjustment

- \* Do not to forget to connect the dummy antenna (terminator) before operating the transmitter.
- \* Do not depress the PTT switch during receiver check. If the transmitter is accidentally operated while a signal generator is connected to the antenna terminal, the signal generator may be damaged.
- \* Transmitter output power should be checked at rated power supply voltage.
- \* Transmitter adjustment should be done on channel 16.

## 2-3. RF Output Power

Battery voltage		12V	9.6V	7.2V	7.2 - 12V
Output power	Nominal	5W	3W	2W	1W
	Ratings	4.5 - 5W	2.5 - 3.3W	1.8 - 2.4W	0.7 - 0.95W
	Reduced	below 1W			0.5W

1. Connect RF power meter to the antenna terminal.
2. Press the HI-LOW button to select "high power".
3. Depress the PTT switch and adjust RV201 on RF board so that the meter indicates rated output power of the unit.
4. Press again the HI-LOW button to select "low power".
5. Adjust RV202 to below 1W.

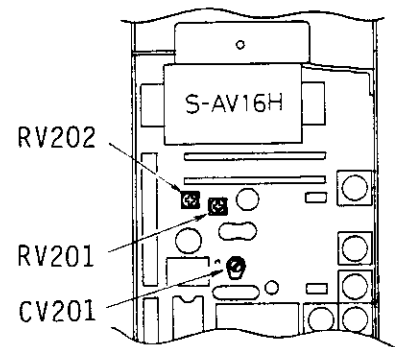


Fig.2-1

## 2-4. Carrier Frequency

1. Connect the directional coupler and frequency counter to the antenna plug.
2. Press the PTT switch and adjust CV201 on the RF board for rated frequency (tolerance:  $156.8\text{MHz} \pm 1.254\text{kHz}$ ).

## 2-5. Modulation

1. Connect the distortion meter to the antenna plug thru the directional coupler.
2. Connect the AF oscillator to the mic input and apply an audio signal of  $-35\text{dBm} \pm 4\text{dBm}$  ( $0\text{dBm} = 1\text{mW}/600\text{ ohms}$ ).
3. Adjust RV501 for modulation of 60%.
4. Increase the SG output by 20 dB and adjust RV502 so that the modulation is 100% or below for an audio input from 300 to 3000Hz.

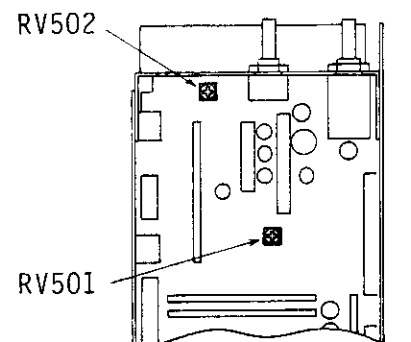


Fig.2-2

## 2-6. Receiver Sensitivity

1. Connect the distortion meter across the speaker (or 8 ohm resistive load).
2. Connect the VHF/FM signal generator, set to 30dBu modulated signal (1kHz, 60%), to the antenna terminal.
3. Adjust frequency of signal generator for max. AF output.
4. Gradually decrease the output level of the signal generator to approx. -10dBu, and confirm that SND/ND ratio of more than 12dB is obtained.

## 2-7. Disassembling the Unit

1. Push the release switch upward and slide off the battery pack from the body.
2. Unscrew the five panhead screws (a) thru (e) and remove the rear panel.
3. Take out the battery pack guide plate by loosening the four flathead screws.
4. Holding the unit with the front panel facing downward, press the battery contact to detach the main chassis from the front panel (although the wires are still connected between the front panel and the main chassis).
5. To unlock the stopper, disconnect plugs A and B while pushing out the catches at both sides of the jacks.
6. Unscrew the four flat-head screws (f) thru (i), disconnect the plugs C and D in the same manner as plugs A and B, then separate the RF board from AF board assemblies.

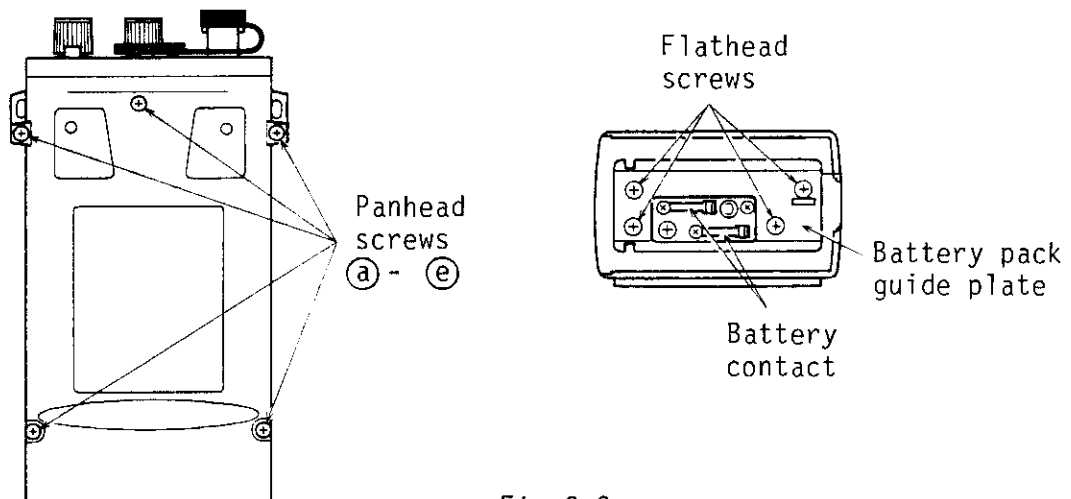
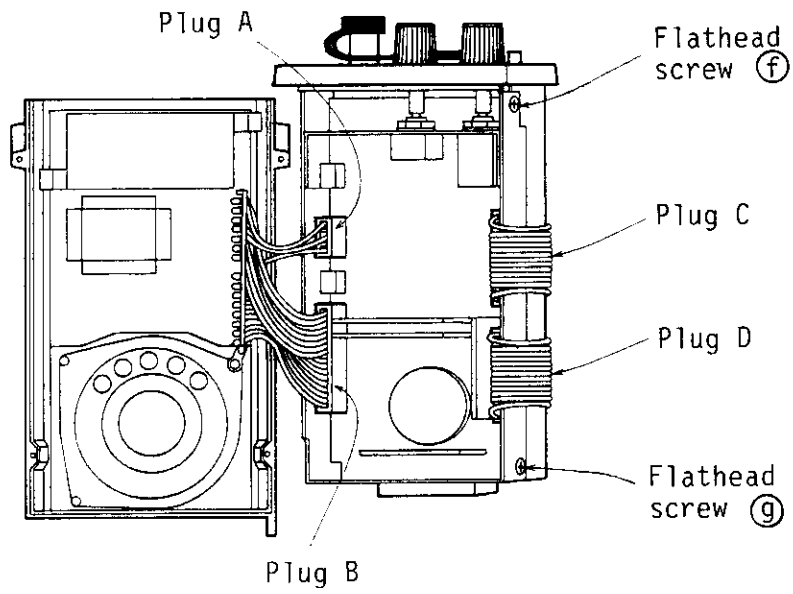


Fig.2-3





Flathead screws (h) and (i) are located on the opposite side of (f) and (g).

Fig.2-4

## CHAPTER 3. CHANGE OF SPECIFICATIONS

The FM-55 is available in 3 versions: international, USA and special versions. Where permitted, these versions may be locally modified by cutting or adding the jumper links on the CPU board as follows.

### CAUTION

This instructions is prepared to be used by authorized FURUNO agents or dealers to preset the equipment to comply fully with their local regulations and minor restrictions. Please carefully read the instructions and follow the recommended procedures for preset operations.

FURUNO will assure no responsibility for inconvenience or disturbance to communications due to inadequate or unlowful presetting of the equipment.

Please note again that the preset must be done by the authorized dealer, not by the operator or owner of the equipmet.

### 3-1. Change of Specifications

1. Switch off the transceiver and remove the front panel by loosening the nine fixing screws. As for the disassembling procedure, see page 2-3, section 2-7, "Disassembling the Unit".
2. There are three link spots at the left corner on the rear of the front panel as illustrated below. These spots are opened or shorted according to whether you need AUTO SCAN, PRIVATE CHANNELS and USA/INT switchover.

SPOT	STATUS	FUNCTION
U/I	short	USA/INT switchover, Weather mode accessible
	open	Beep sound on/off selectable
P	short	Private mode inhibited
	open	Private mode accessible
S	short	Auto scanning disabled
	open	Auto scanning activated

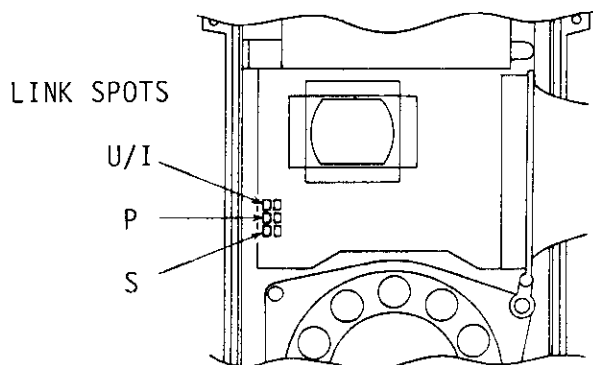


Fig.3-1 CPU Board

## 3-2. Programming Private Channel

A maximum 10 private channels are programmable. These channels must be programmed according to the private channel list on page v.

### Programming procedure

1. For the sets before S/No. 516-0030, cut the link spot P on the CPU board to enable private channel selection. This change is not necessary for the sets having S/No. 516-0031 and after. (They are delivered with this link open.)
2. Find the socket for private channelization on the RF board. See Fig. 3-2. The socket must be set as in Fig. 3-2(b). Replace the socket as in Fig. 3-2(a).
3. Turn on the power. The transceiver is now put in the private channel mode, and the legend "PRV", private address "1", and the contents of the address are displayed. When no channel is programmed yet, "-" will be displayed.
4. Enter the desired private channel number in 2 digits selected from the private channel list. The channel numbers "A", "B", and "C" are represented by "16", "MR" and "X" keys, respectively.
5. Move to the desired private address with the [▲] or [▼] keys. Repeat step 4.
6. Return the socket as in Fig. 3-2(b). Assemble the equipment, and the transceiver can be operated on the loaded private channels according to the operator's manual.

Note: Transmission is inhibited while private channels are programmed.

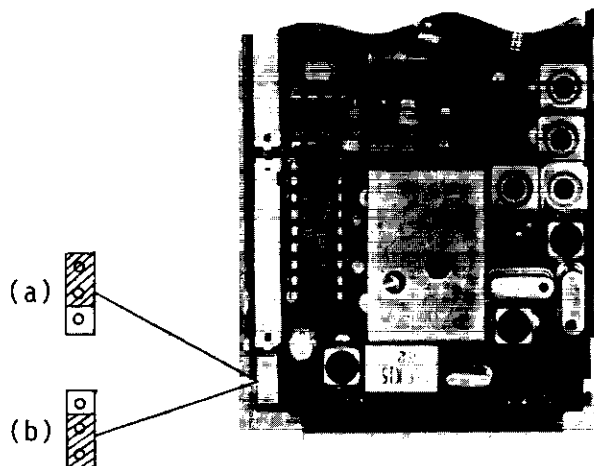
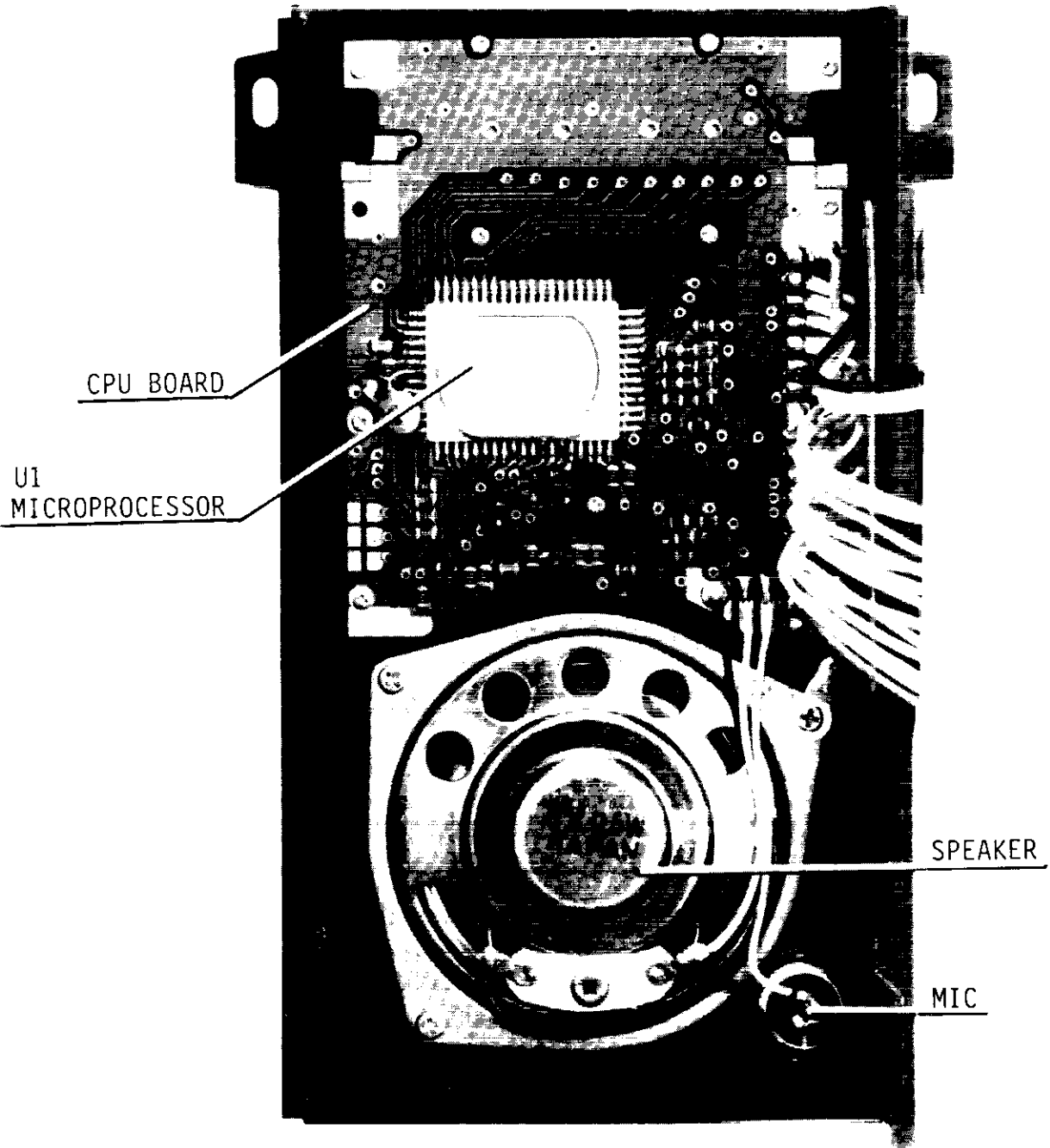


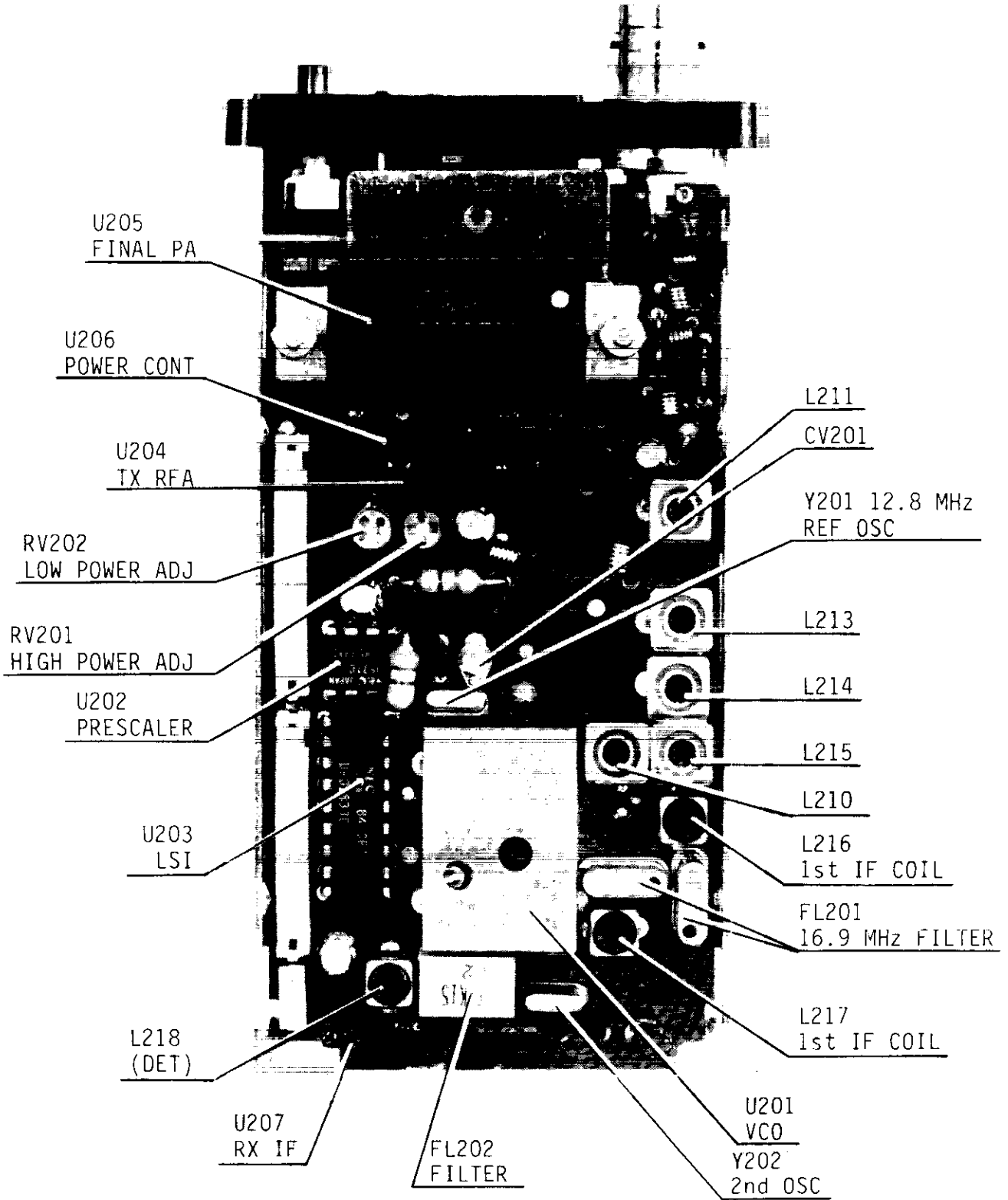
Fig.3-2 RF Board

CHAPTER 4. PARTS LOCATION

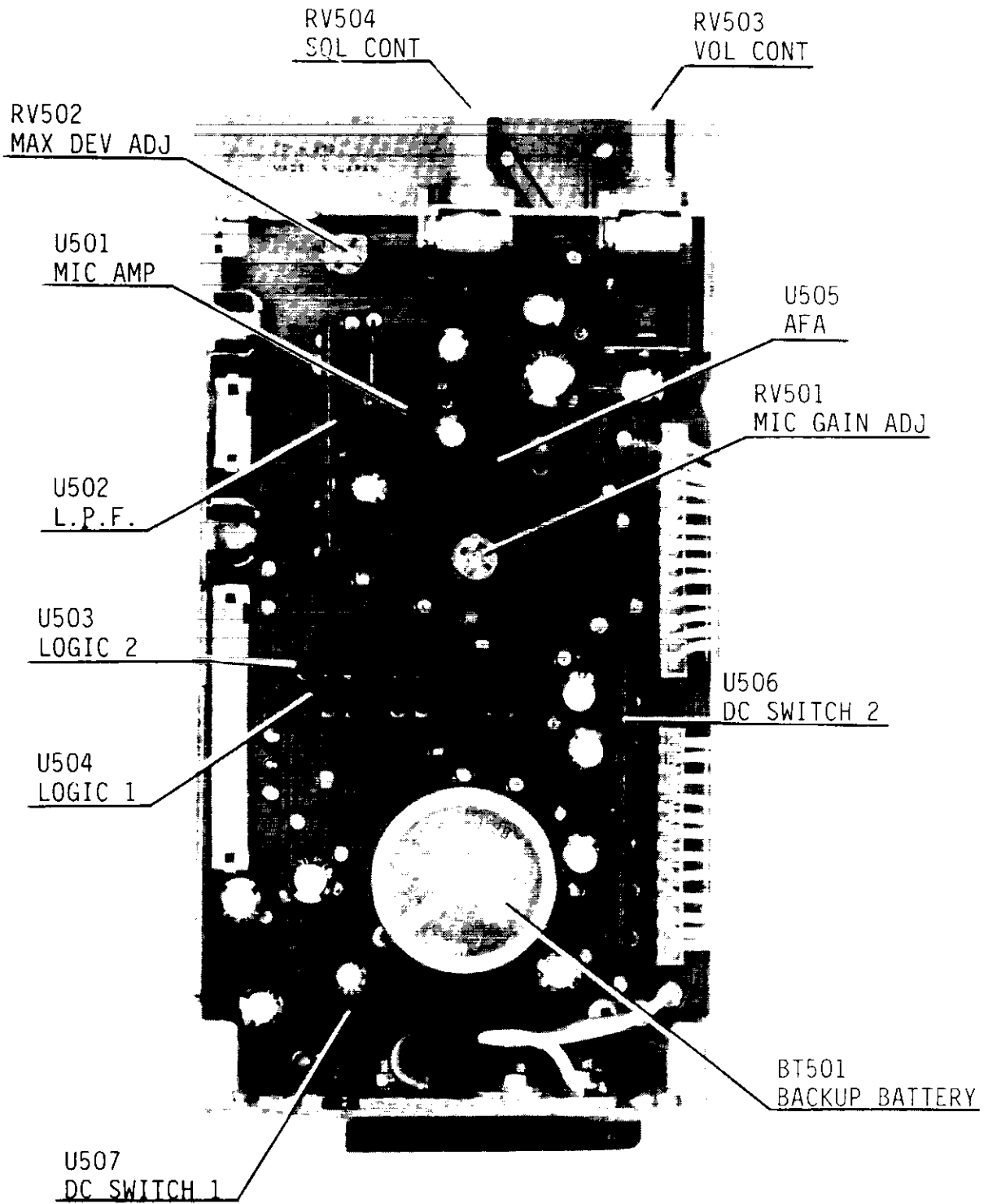
4-1. Front Chassis Rear View (CPU Board)



## 4-2. RF Board



4-3. AF Board



## CHAPTER 5. TECHNICAL DATA

This chapter describes the major function and block diagram of the semiconductors used in FM-55, to assist circuit analysis and check.

The manufacturer on the list is abbreviated as follows.

NEC : Nippon Electric  
 TOS : Toshiba  
 SEK : Seiko  
 TAM : Tama

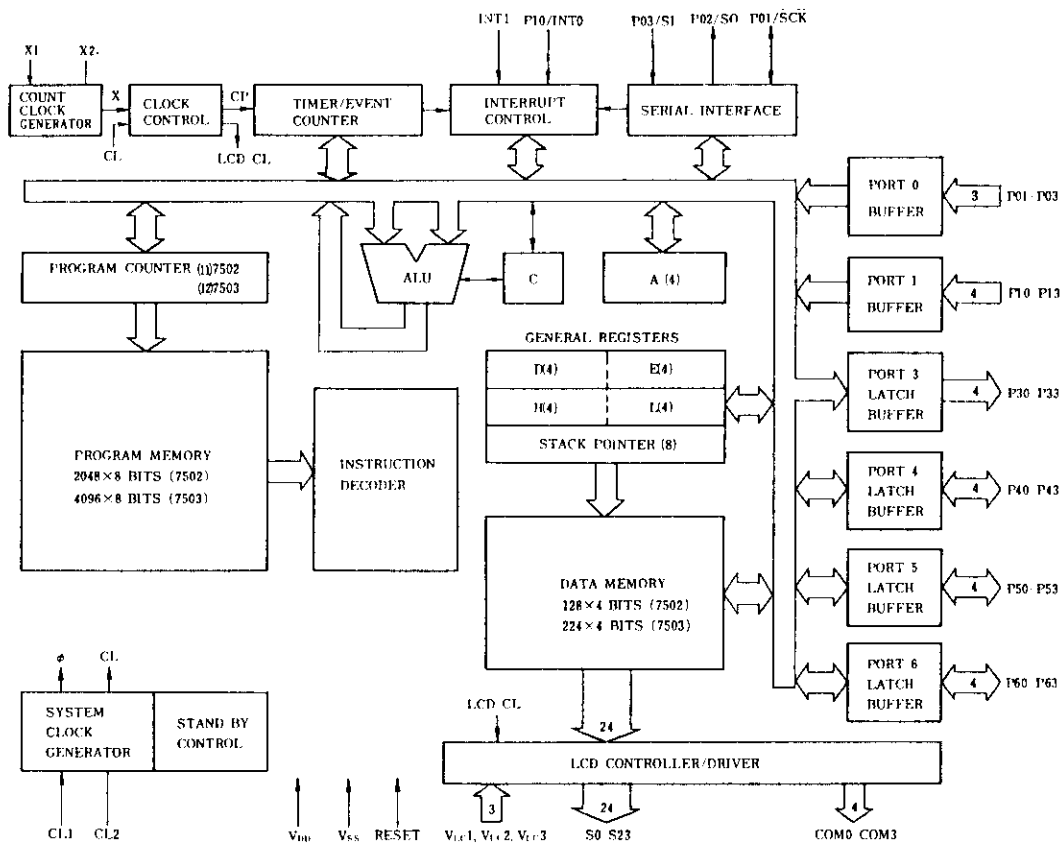
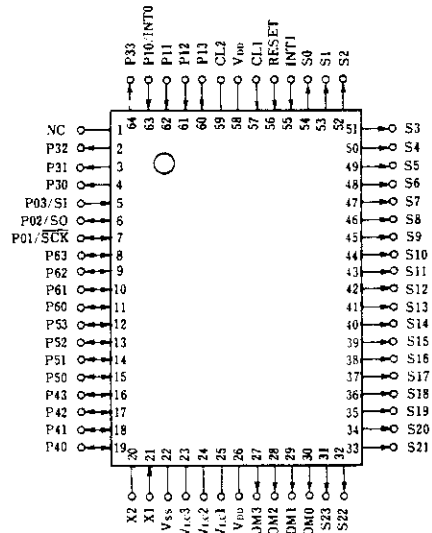
SYMBOL	TYPE	FUNCTION	MFR	CODE NO.
U101	uPD7503	Microprocessor	NEC	6036750302
U201	TA51-3029	VCO	TAM	0051302901
U202	uPB571C	Prescaler	NEC	6036057101
U203	uPD2833C	PLL	NEC	6036283301
U204	TA51-3022	TX RF amplifier	TAM	0051302201
U205	S-AV16H	RF power amplifier	TOS	6012001601
U206	TA51-3023K1	Power controller, for 5-2W	TAM	0051302301
	TA51-3023K2	" for 1W	TAM	0051302302
	TA51-3023K3	" for 2W	TAM	0051302303
U207	TA51-3031	IF amplifier	TAM	0051303101
U501	TA7140P	Mic amplifier	TOS	6012714001
U502	TA51-3024	Low-pass filter	TAM	0051302401
U503	TA51-3028	Logic (2)	TAM	0051302801
U504	TA51-3027	Logic (1)	TAM	0051302701
U505	TA7368P	Linear amplifier	TOS	6012736801
U506	TA51-3026	DC switch (2)	TAM	0051302601
U507	TA51-3025	DC switch (1)	TAM	0051302501
U508	S-81250HG	Regulator	SEK	6032812501
U601	T302001	LCD	SEK	3038302001

U101 uPD7503

## Single Chip 4-bit Microprocessor

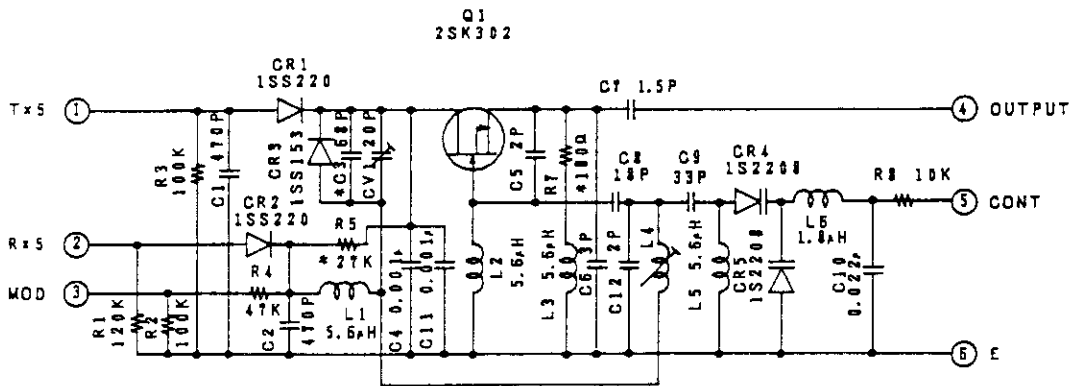
- 4-bit parallel processing
- 92 instructions
- Instruction cycle: 10 us/200 kHz
- Program Memory
  - ROM: 4096 x 8 bits
- Data Memory
  - RAM: 224 x 4 bits
- Interrupt functions:
  - 2 x external, 2 x internal
- 8-bit timer/event counter
- 8-bit serial interface
- 23 i/o lines
- LCD controller/driver
  - Segment outputs: 24
  - Common outputs: 4

(Top View)

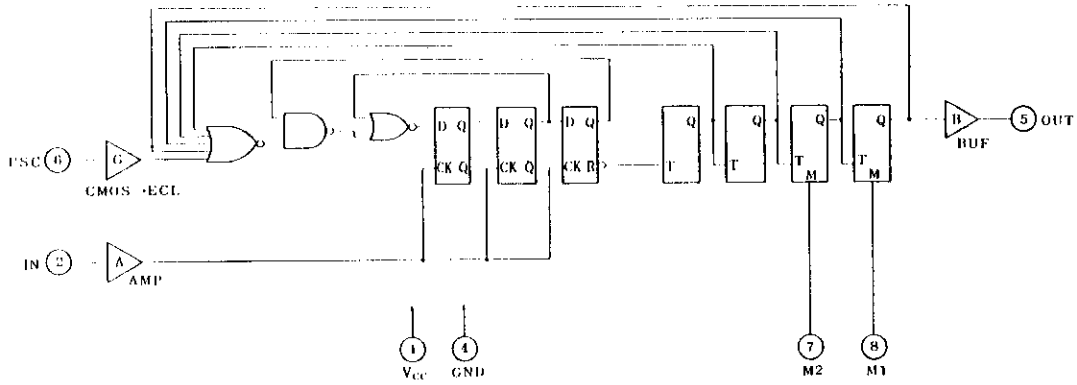




## U201 TA51-3029

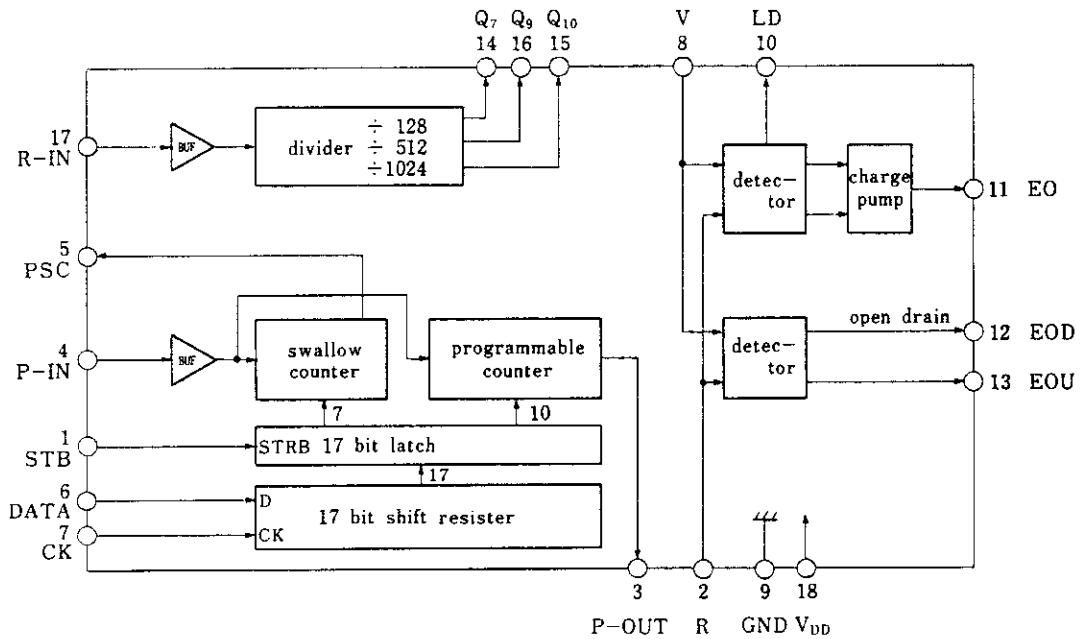


## U202 uPB571C

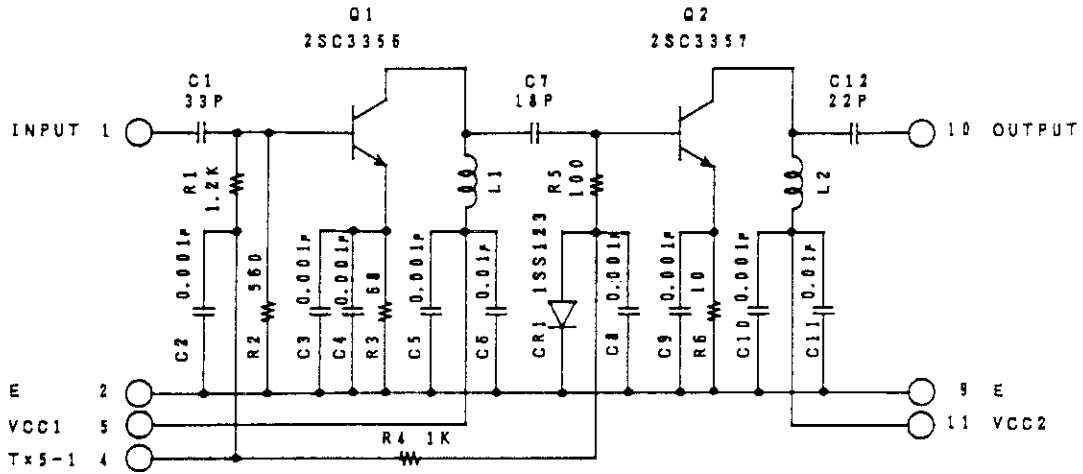


## U203 uPD2833C

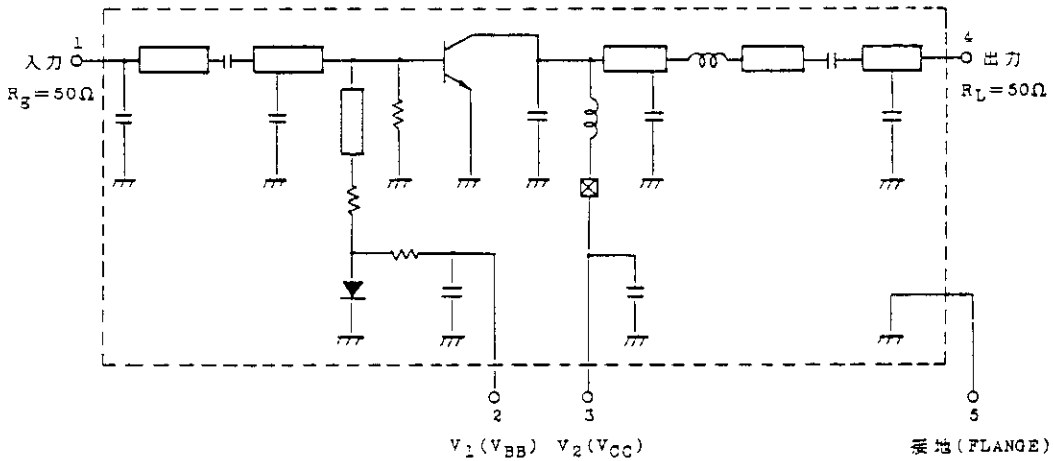
### Block Diagram



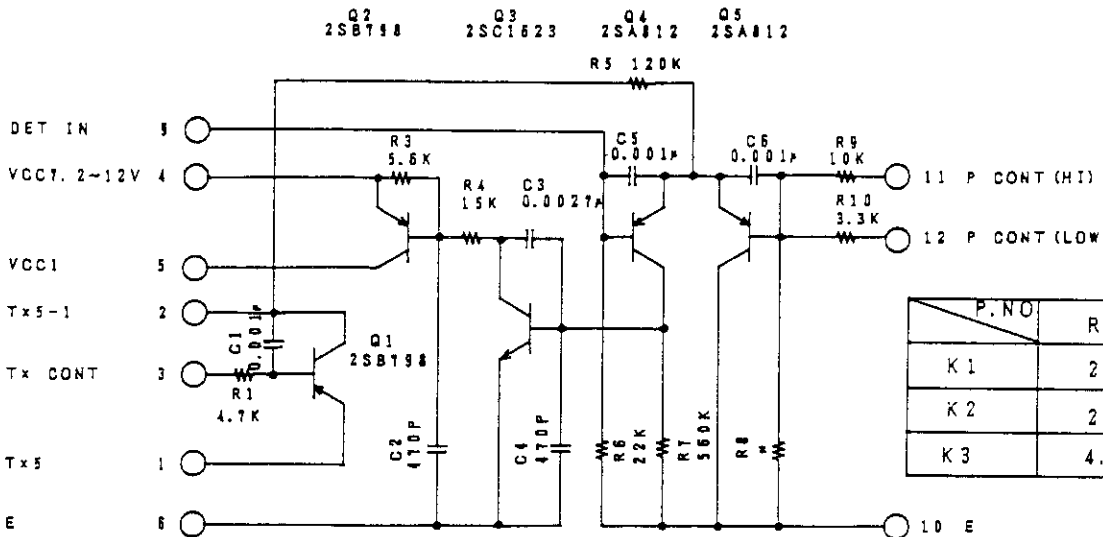
U204 TA51-3022



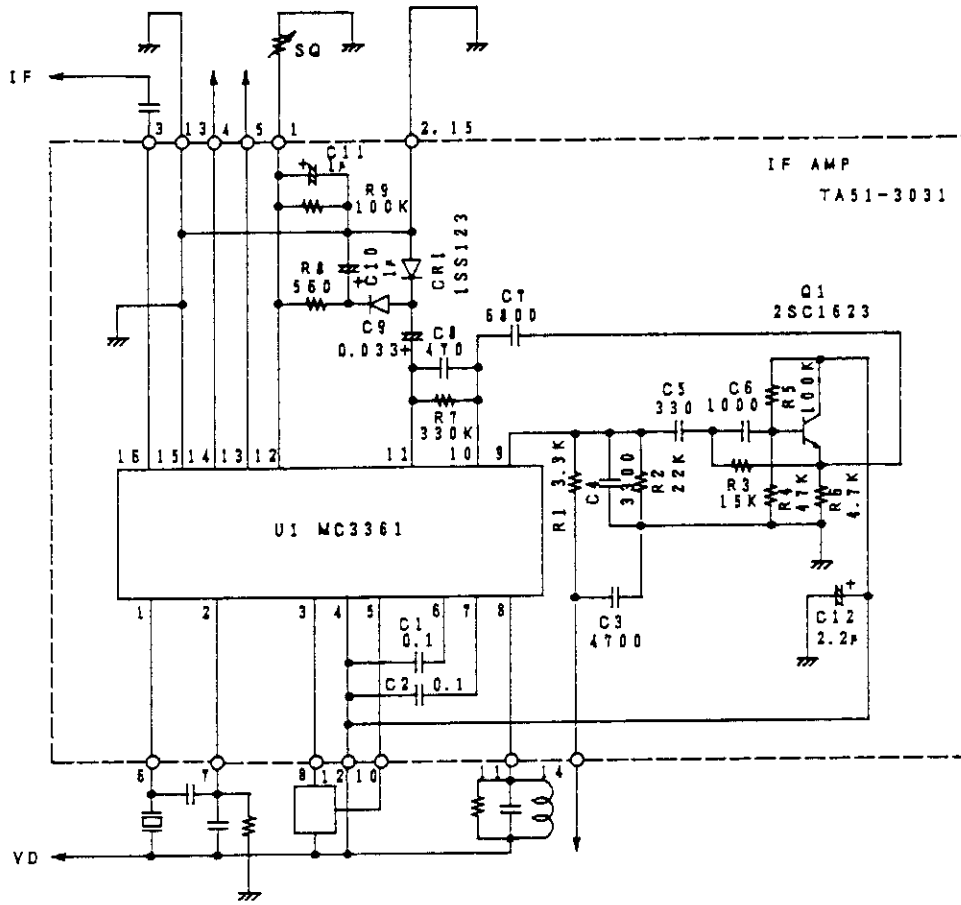
U205 S-AV16H



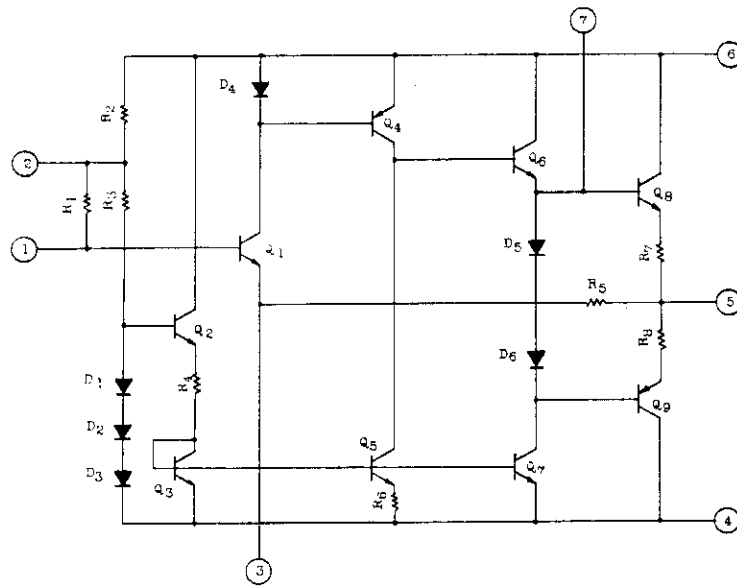
U206 TA51-3023K1



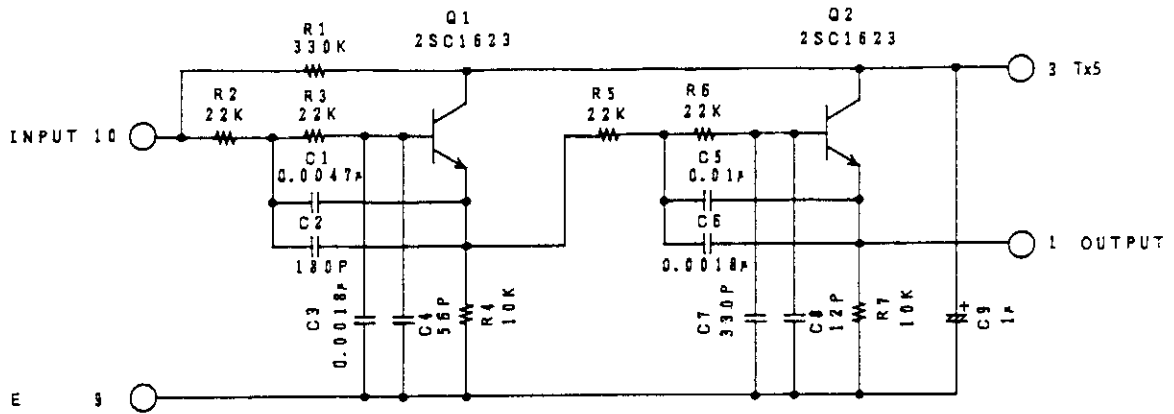
U207 TA51-3031



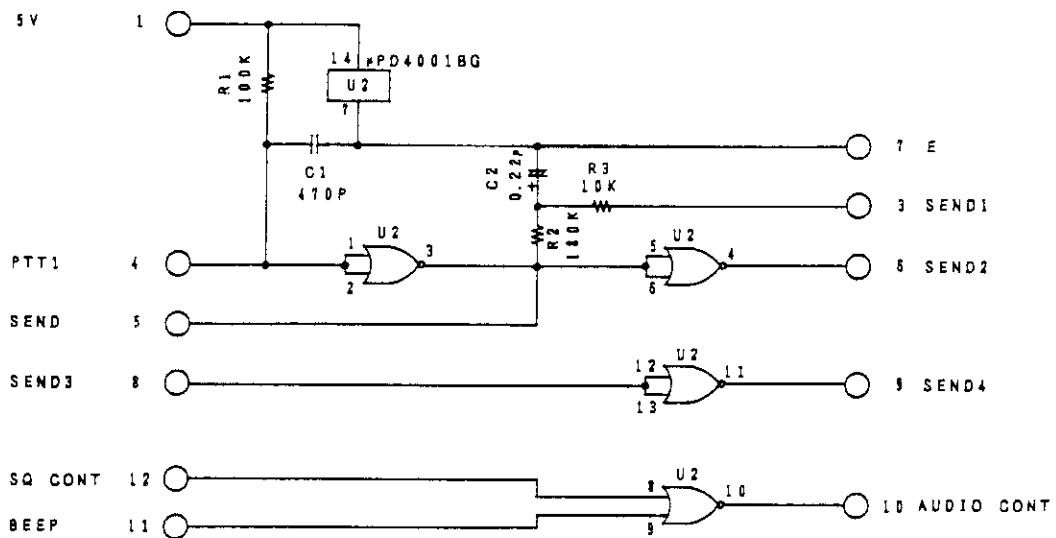
U501 TA7140P



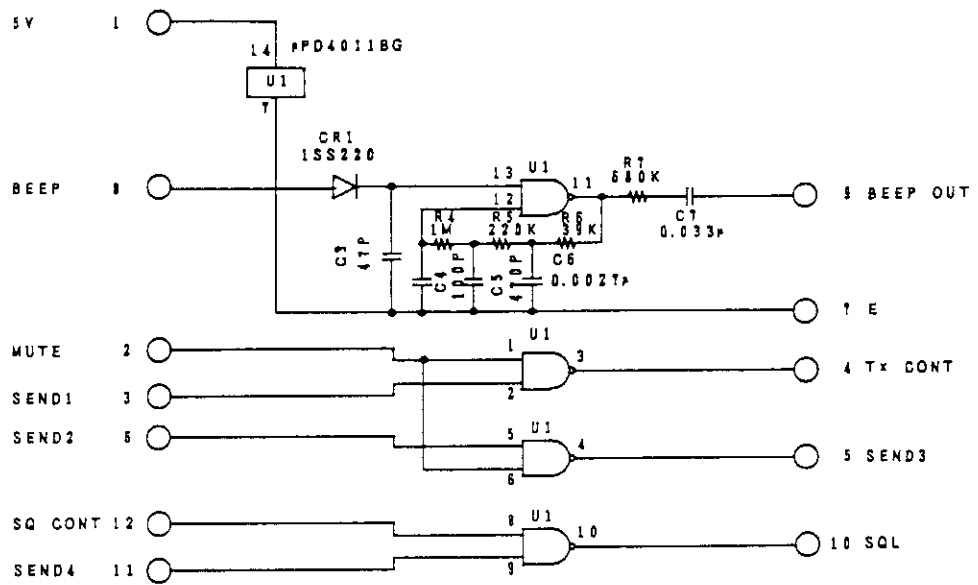
U502 TA51-3024



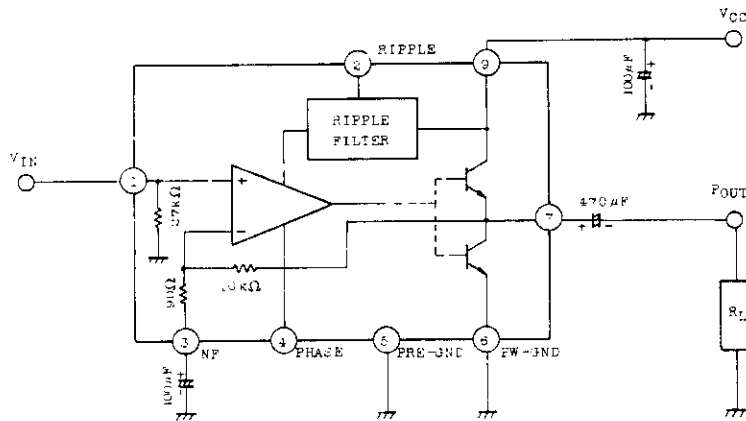
U503 TA51-3028



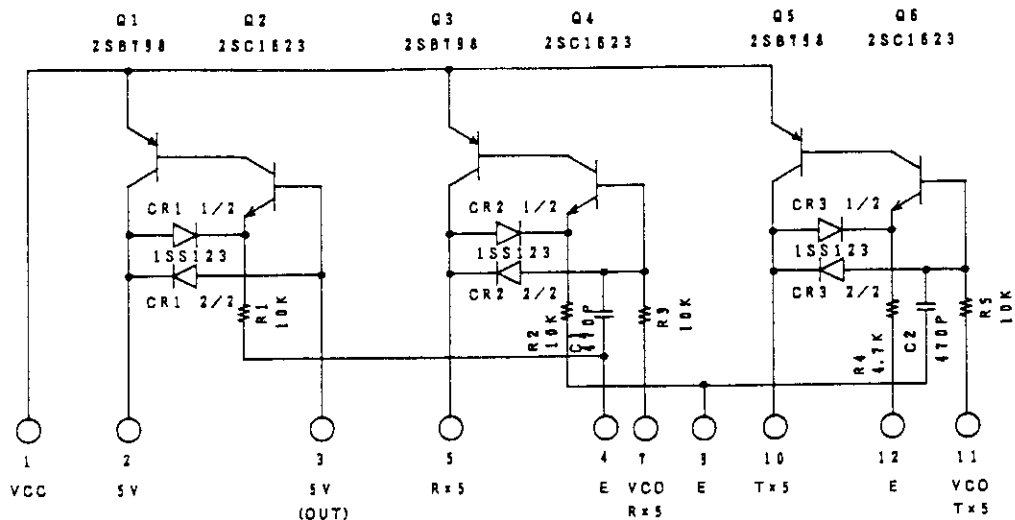
U504 TA51-3027



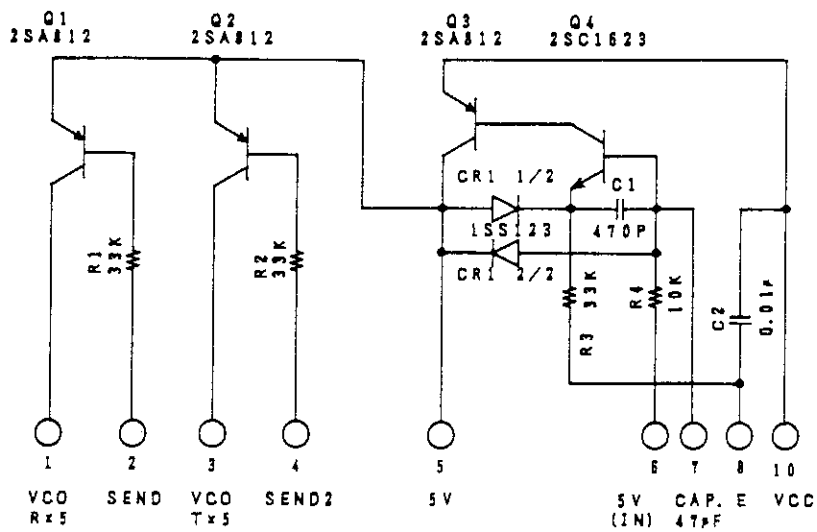
U505 TA7368P



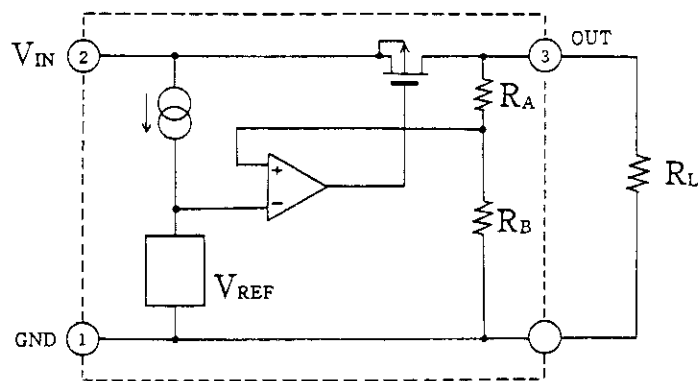
U506 TA51-3026



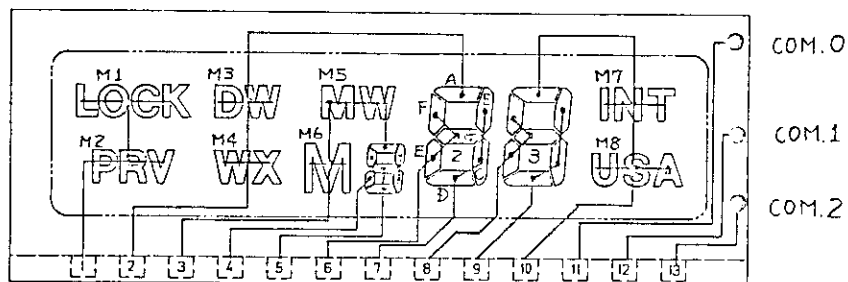
U507 TA51-3025



U508 S-81250HG



U601 T302001



FM-55 MAINTENANCE PARTS LIST

NO.	NAME	TYPE	CODE NO.	Q'TY	REMARKS
1	SPEAKER	S45G03-3	7036450301	1	
2	KNOB	TA11-1345	0011134501	1	
3	REAR CASE	TC11-1340	0011134001	1	with gasket
4	POTENTIOMETER	RK0941111 20kohm	1015200302	1	
5	POTENTIOMETER	RK0941110 100kohm	1015100401	1	
6	LITIUM BATTERY	CR2032T2	7010203201	1	
7	ANTENNA	TA11-6044	0011604401	1	
8	GASKET	TA11-1368K2	0011136802	1	
9	FRONT CASE ASSY	TA11-3069K1	0011306901	1	
	Front case				
	CPU board				
	Speaker				
	PTT switch				
	LCD				
10	CHASSIS A ASSY	TA11-3068K1	0011306801	1	
	Chassis A				
	RF board				
	Top panel				
11	CHASSIS B ASSY	TA11-3067	0011306701	1	
	Chassis B				
	AF board				

SYMBOL 記 号	CODE NO. コード番号	TYPE 型 名	SYMBOL 記 号	CODE NO. コード番号	TYPE 型 名
CPU BOARD (Dwg. No. C5460-004-A)			R 105	1004300301	RMC1/10 30KaJ
			R 106	=R 104	-
			R 107	=R 104	-
			R 108	=R 104	-
			R 109	=R 104	-
			R 110	=R 104	-
			R 111	=R 104	-
			R 112	=R 104	-
			R 113	1004160401	RMC1/10 160KaJ
			R 114	=R 101	-
			R 115	=R 101	-
			R 116	=R 101	-
			R 117	1004120301	RMC1/10 12KaJ
			R 118	=R 104	-
			R 119	=R 104	-
			R 120	=R 104	-
			R 121	=R 104	-
CAPACITOR			INTEGRATED CIRCUIT		
C 101	2004470111	C3K21F1HR471K	U 101	6036750302	#PD7503G-710-12
C 102	=C 101	-			
C 103	=C 101	-			
C 104	2004100311	C3K21F1HR103K	CAPACITOR		
C 105	2004330001	C2C21F1HC6330J	C 601	2004470101	C2C21F1HC6471J
C 106	=C 104	-	SPEAKER		
C 107	=C 104	-	LS601	7036450301	S45603-3
C 108	=C 104	-	MICROPHONE		
C 109	=C 104	-	MK601	7037008001	EM80
C 110	=C 104	-			
DIODE					
CR101	5008047003	RD4.7MB3			
CR102	5001283501	1S2835			
CR103	=CR102	-			
CR104	=CR102	-			
CR105	=CR102	-			
CR106	=CR102	-			
LAMP					
DS101	7026000101	RM3-5V30			
DS102	=DS101	-			
TRANSISTOR					
Q 101	4011162301	2SC1623L4			
Q 102	4009081201	2SA812M4			
R 101	1004100301	RMC1/10 10KaJ			
R 102	=R 101	-			
R 103	1004100501	RMC1/10 1MaJ			
R 104	1004100401	RMC1/10 100KaJ			

 NOTE:  
 備 考:



SYMBOL 記 号	CODE NO. コード番号	TYPE 型 名	SYMBOL 記 号	CODE NO. コード番号	TYPE 型 名
RF BOARD (Dwg. No. C5460-003-A)			C 237	=C 233	-
			C 239	=C 208	-
			C 240	=C 234	-
			C 241	=C 232	-
			C 242	=C 208	-
			C 243	=C 232	-
			C 244	2004300001	C2C21F1HCG300J
			C 245	2004020001	C2C21F1HCG020C
			C 246	=C 244	-
			C 247	2002150013	HE40SJCH150K
			C 249	=C 224	-
			C 250	=C 222	-
			C 251	=C 234	-
			C 252	2004030001	C2C21F1HCG030C
			C 253	=C 208	-
			C 254	2021100003	USA1C100MCA
			C 255	=C 208	-
			C 256	=C 211	-
			C 257	=C 211	-
			C 259	=C 244	-
			C 260	=C 204	-
			C 261	=C 252	-
			C 262	=C 234	-
			C 263	=C 208	-
			C 264	=C 211	-
			C 265	=C 252	-
			C 266	=C 228	-
			C 267	2004010001	C2C21F1HCG010C
			C 268	=C 233	-
			C 269	2004015001	C2C21F1HCG1R5C
			C 270	=C 228	-
			C 271	=C 205	-
			C 274	=C 205	-
			C 275	=C 234	-
			C 277	=C 224	-
			C 278	2004470211	C3K21F1HR472K
			C 279	=C 278	-
			C 280	=C 205	-
CAPACITOR					
C 201	2004220311	C3K21F1HR223K			
C 202	=C 201	-			
C 203	2006100602	S991C106ME1			
C 204	2004040001	C2C21F1HCG040C			
C 205	2004100311	C3K21F1HR103K			
C 206	2020100104	USR1C101MCA			
C 207	=C 205	-			
C 208	2004100211	C3K21F1HR102K			
C 209	2008100502	SVA1C105M1			
C 211	2004470111	C3K21F1HR471K			
C 212	=C 211	-			
C 213	=C 211	-			
C 214	=C 208	-			
C 215	2004070001	C2C21F1HCG070C			
C 216	2004270001	C2C21F1HCG270J			
C 217	2004220102	C2C31F1HCG221J			
C 218	2004430001	C2C21F1HCG430J			
C 219	=C 205	-			
C 220	=C 205	-			
C 221	2004180001	C2C21F1HCG180J			
C 222	2004100101	C2C21F1HCG101J			
C 223	=C 208	-			
C 224	2004240001	C2C21F1HCG240J			
C 225	=C 208	-			
C 226	2004200001	C2C21F1HCG200J			
C 227	=C 208	-			
C 228	2004080001	C2C21F1HCG080C			
C 229	=C 208	-			
C 230	2020470004	USR1C470MCA			
C 232	2004150001	C2C21F1HCG150J			
C 233	2004100001	C2C21F1HCG100J			
C 234	2004120001	C2C21F1HCG120J			
C 235	=C 233	-			
C 236	=C 234	-			

 NOTE:  
 備 考:

SYMBOL 記 号	CODE NO. コード番号	TYPE 型 名	SYMBOL 記 号	CODE NO. コード番号	TYPE 型 名
C 281	=C 208	-	COIL		
C 282	2004390001	C2C21F1HC6390J	L 201	3008245501	2455L
C 283	2004560001	C2C21F1HC6560J	L 202	3003470102	LAL04NA471K
C 285	=C 205	-	L 203	3008244501	2445L
C 286	=C 230	-	L 204	3003056012	LAL03NA5R6K
C 287	2008100405	SVA1V104M1	L 205	3008304501	3045L
C 289	=C 211	-	L 206	=L 203	-
C 290	=C 211	-	L 207	=L 203	-
C 291	=C 232	-	L 208	=L 203	-
C 292	=C 233	-	L 209	3005339501	07P3395N
C 293	=C 226	-	L 210	3005700331	07P003TS
DIODE			L 211	=L 201	-
CR201	5001015301	1S8153	L 212	3005453002	07P4530Y
CR202	=CR201	-	L 213	=L 212	-
CR203	=CR201	-	L 214	=L 212	-
CR204	=CR201	-	L 215	3005504411	05E044T
CR205	5009030101	MI301	L 216	=L 215	-
CR206	=CR205	-	L 217	3005504512	05E045L
CR207	5001220801	1S2208	TRANSISTOR		
CR208	5004211001	SEL2110R	Q 201	4011222301	2SC2223F13
CR209	=CR207	-	Q 202	4011335601	2SC3356
CR210	=CR201	-	Q 203	=Q 202	-
VARIABLE CAPACITOR			Q 204	4011162301	2SC1623L4
CV201	2019100101	CV38D1001	Q 205	=Q 204	-
FILTER			Q 206	=Q 202	-
FL201	7034161502	16F15B	Q 207	4016030201	2SK3026R
FL202	7035001501	CLF-K15	Q 208	=Q 201	-
JACK			Q 209	=Q 201	-
J 201	7033083601	HSJ0836-01-310	Q 210	=Q 201	-
J 202	7033110201	HSJ1102-01-010	RESISTOR		
J 203	7030001201	TVL-F12F-D1	R 201	1004100301	RMC1/10 10K $\Omega$ J
J 204	7030001301	TVL-F13F-D1	R 202	=R 201	-
			R 203	1004470101	RMC1/10 470 $\Omega$ J
			R 204	1004100201	RMC1/10 1K $\Omega$ J
			R 205	1004220401	RMC1/10 220K $\Omega$ J

 NOTE:  
 備 考:

SYMBOL 記 号	CODE NO. コード番号	TYPE 型 名	SYMBOL 記 号	CODE NO. コード番号	TYPE 型 名
R 207	1004820301	RMC1/10 82K $\Omega$ J	R 248	1004680301	RMC1/10 68K $\Omega$ J
R 208	1004820201	RMC1/10 8.2K $\Omega$ J	R 249	1004270301	RMC1/10 27K $\Omega$ J
R 209	1004220301	RMC1/10 22K $\Omega$ J	R 250	=R 201	-
R 210	1004100101	RMC1/10 100 $\Omega$ J	THERMISTOR		
R 211	1004150201	RMC1/10 1.5K $\Omega$ J	RT201	1014512001	TD5-A120D
R 212	1004470201	RMC1/10 4.7K $\Omega$ J	POTENTIOMETER		
R 213	1004560201	RMC1/10 5.6K $\Omega$ J	RV201	1011220301	H0521A22K $\Omega$ B
R 214	1004270101	RMC1/10 270 $\Omega$ J	RV202	1011220201	H0521A2.2K $\Omega$ B
R 215	=R 204	-	SWITCH		
R 216	=R 201	-	S 201	7032220001	SPPH22
R 217	=R 209	-	INTEGRATED CIRCUIT		
R 218	=R 201	-	U 201	0051302901	TA51-3029
R 219	1004220101	RMC1/10 220 $\Omega$ J	U 202	6036057101	MPB571C
R 220	=R 212	-	U 203	6036283301	MPD2833C
R 221	=R 212	-	U 204	0051302201	TA51-3022
R 222	=R 201	-	U 205	6012001601	S-AV16H
R 223	=R 203	-	U 206-1	0051302301	TA51-3023K1 *1
R 224	1004470301	RMC1/10 47K $\Omega$ J	U 206-2	0051302302	TA51-3023K2 *2
R 225	=R 209	-	U 206-3	0051302303	TA51-3023K3 *3
R 226	1004680201	RMC1/10 6.8K $\Omega$ J	U 207	0051303101	TA51-3031
R 227	=R 203	-	CRYSTAL		
R 228	1004100401	RMC1/10 100K $\Omega$ J	Y 201	0011604701	TA11-6047
R 229	=R 210	-	Y 202	0011604801	TA11-6048
R 232	=R 213	-			
R 233	1004270201	RMC1/10 2.7K $\Omega$ J			
R 234	=R 201	-			
R 235	1004560401	RMC1/10 560K $\Omega$ J			
R 236	=R 224	-			
R 237	1004560101	RMC1/10 560 $\Omega$ J			
R 238	=R 219	-			
R 239	=R 211	-			
R 240	1004470001	RMC1/10 47 $\Omega$ J			
R 241	=R 204	-			
R 242	1004022002	RMC1/10 2.2 $\Omega$ K			
R 243	=R 224	-			
R 244	1004330401	RMC1/10 330K $\Omega$ J			
R 245	=R 212	-			
R 246	=R 208	-			
R 247	=R 242	-			

NOTE:  
備考: \*1: 5 to 2W  
\*2: 1W  
\*3: 2W

SYMBOL 記 号	CODE NO. コ-ド番号	TYPE 型 名	SYMBOL 記 号	CODE NO. コ-ド番号	TYPE 型 名
AF BOARD (Dwg. No. C5460-005-A)			C 535	2006100503	S991E105MA1
			C 536	=C 505	-
			C 537	=C 502	-
			C 538	=C 502	-
BATTERY			C 539	2020330005	USR1E330MCA
BT501	7010203201	CR2032T2	C 540	=C 501	-
CAPACITOR			C 541	=C 539	-
C 501	2004100311	C3K21F1HR103K	C 542	=C 502	-
C 502	2004470111	C3K21F1HR471K	DIODE		
C 504	=C 502	-	CR501	5001283501	1S2835
C 505	2021100003	USA1C100MCA	CR502	5001283701	1S2837
C 506	2024470602	S891A476MD1	CR503	=CR502	-
C 507	2004330211	C3K21F1HR332K	CR504	=CR502	-
C 508	=C 505	-	CR505	=CR502	-
C 509	2008100502	SVA1C105M1	CR506	5008091001	RD9.1MB1
C 511	=C 509	-	CR507	=CR502	-
C 512	=C 505	-	CR508	5001012301	1S5123
C 513	=C 509	-	JACK		
C 514	=C 501	-	J 501	7030000301	TVL-F03F-D1
C 515	=C 509	-	J 502	7030001301	TVL-F13F-D1
C 516	2020470004	USR1C470MCA	TRANSISTOR		
C 517	=C 501	-	Q 501	4011162301	2SC1623L4
C 518	=C 501	-	Q 502	=Q 501	-
C 520	2008100405	SVA1V104M1	Q 503	=Q 501	-
C 521	=C 520	-	Q 504	=Q 501	-
C 522	=C 516	-	Q 505	4010079811	2SB798DM
C 523	2004470211	C3K21F1HR472K	Q 506	4009081201	2SAB12M4
C 524	2006100602	S991C106ME1	RESISTOR		
C 525	2004470001	C2C21F1HCG470J	R 501	1004100001	RMC1/10 10nJ
C 526	2020100104	USR1C101MCA	R 502	1004680201	RMC1/10 6.8KnJ
C 527	=C 501	-	R 503	1004300301	RMC1/10 30KnJ
C 528	=C 516	-	R 504	1004220401	RMC1/10 220KnJ
C 529	=C 520	-	R 505	1004820201	RMC1/10 8.2KnJ
C 531	=C 516	-			
C 532	=C 516	-			
C 533	=C 516	-			
C 534	=C 516	-			

 NOTE:  
 備 考:

SYMBOL 記号	CODE NO. コード番号	TYPE 型名	SYMBOL 記号	CODE NO. コード番号	TYPE 型名
R 506	=R 504	-	INTEGRATED CIRCUIT		
R 507	1004100201	RMC1/10 1K $\Omega$ J	U 501	6012714001	TA7140P
R 508	1004270201	RMC1/10 2.7K $\Omega$ J	U 502	0051302401	TA51-3024
R 510	1004100401	RMC1/10 100K $\Omega$ J	U 503	0051302801	TA51-3028
R 511	=R 510	-	U 504	0051302701	TA51-3027
R 512	=R 510	-	U 505	6012736801	TA7368P
R 514	=R 510	-	U 506	0051302601	TA51-3026
R 515	1004330401	RMC1/10 330K $\Omega$ J	U 507	0051302501	TA51-3025
R 516	1004100101	RMC1/10 100 $\Omega$ J	U 508	6032812501	S-81250HG
R 517	1004120201	RMC1/10 1.2K $\Omega$ J			
R 518	1004560201	RMC1/10 5.6K $\Omega$ J			
R 520	=R 516	-			
R 521	1004470301	RMC1/10 47K $\Omega$ J			
R 522	1004120301	RMC1/10 12K $\Omega$ J			
R 523	1004100301	RMC1/10 10K $\Omega$ J			
R 524	=R 523	-			
R 526	=R 523	-			
R 527	1004470101	RMC1/10 470 $\Omega$ J			
R 528	=R 518	-			
R 529	=R 501	-			
THERMISTOR					
RT501	1014031001	TH-C310			
POTENTIOMETER					
RV501	1011220201	H0521A2.2K $\Omega$ B			
RV502	=RV501	-			
RV503	1015200302	RK0941111 20K $\Omega$ B			
RV504	1015100401	RK0941110 100K $\Omega$ B			
SWITCH					
S 501	7032091001	KHH10910			
S 502	=S 501	-			

NOTE:  
備考:

SYMBOL 記 号	CODE NO. コード番号	TYPE 型 名	SYMBOL 記 号	CODE NO. コード番号	TYPE 型 名
TABLETOP QUICK CHARGER (Dwg. No. C5460-009-A)			FUSE HOLDER		
			FX001	7019000101	1P2I)-
CAPACITOR			JACK		
C 001	2012180205	TLB1V102MAA	J 001	7015000501	B5P-SHF-1AA
C 002	2004470211	C3K21P1HR472K			
C 003	2008100601	SVC1A106M	TRANSISTOR		
C 004	=C 002	-	Q 001	4010077201	2SB772Q
C 005	2004100311	C3K21P1HR103K	Q 002	4012056001	2SD560LB
C 006	=C 005	-	Q 003	4011162302	2SC1623L5
DIODE			Q 004	=Q 003	-
CR001	5005124111	U1BZ41	Q 005	=Q 003	-
CR002	=CR001	-	Q 006	=Q 003	-
CR003	=CR001	-	Q 007	4009081202	2SAB12M5
CR004	=CR001	-	Q 008	=Q 003	-
CR005	5001012301	1SS123	Q 009	=Q 003	-
CR006	5001283501	1S2835	Q 010	=Q 003	-
CR007	=CR001	-	Q 011	=Q 003	-
CR008	5008051000	RDS.1MB	Q 012	=Q 003	-
CR009	=CR006	-	Q 013	=Q 003	-
CR010	=CR006	-	RESISTOR		
CR011	=CR008	-	R 001	1004100001	RMC1/10 10k $\Omega$
CR012	=CR006	-	R 002	1004100301	RMC1/10 10k $\Omega$
CR013	=CR005	-	R 003	=R 002	-
CR014	=CR005	-	R 004	=R 002	-
CR015	=CR008	-	R 005	=R 002	-
CR016	5004411001	SEL4110R	R 006	=R 002	-
CR017	5004411001	SEL4110R	R 007	1004300201	RMC1/10 3k $\Omega$
CR018	=CR017	-	R 008	1004300101	RMC1/10 300 $\Omega$
FUSE			R 009	1004062002	RMC1/10 6.2k $\Omega$
F 001	7003000201	*6.4*30 0.5A	R 010	=R 009	-
			R 011	=R 009	-
			R 012	=R 009	-
			R 013	=R 009	-
			R 014	1004100201	RMC1/10 1k $\Omega$
			R 015	=R 014	-

 NOTE:  
 備 考:

SYMBOL 記号	CODE NO. コード番号	TYPE 型名	SYMBOL 記号	CODE NO. コード番号	TYPE 型名
R 016	=R 014	-			
R 017	=R 002	-			
R 018	=R 002	-			
R 019	1004510301	RMC1/10 51KkJ			
R 020	=R 002	-			
R 021	=R 002	-			
R 022	=R 002	-			
R 023	=R 002	-			
R 024	=R 002	-			
R 025	=R 019	-			
R 026	=R 007	-			
R 027	=R 002	-			
R 028	1004270401	RMC1/10 270KkJ			
R 029	=R 008	-			
R 030	=R 014	-			
R 031	1004470401	RMC1/10 470KkJ			
R 032	=R 028	-			
SWITCH					
S 001-1	7029500101	MS-500A-B-RI			
INTEGRATED CIRCUIT					
U 001	6021916002	TC9160F			
U 002	6032805404	S-8054HN-CB			

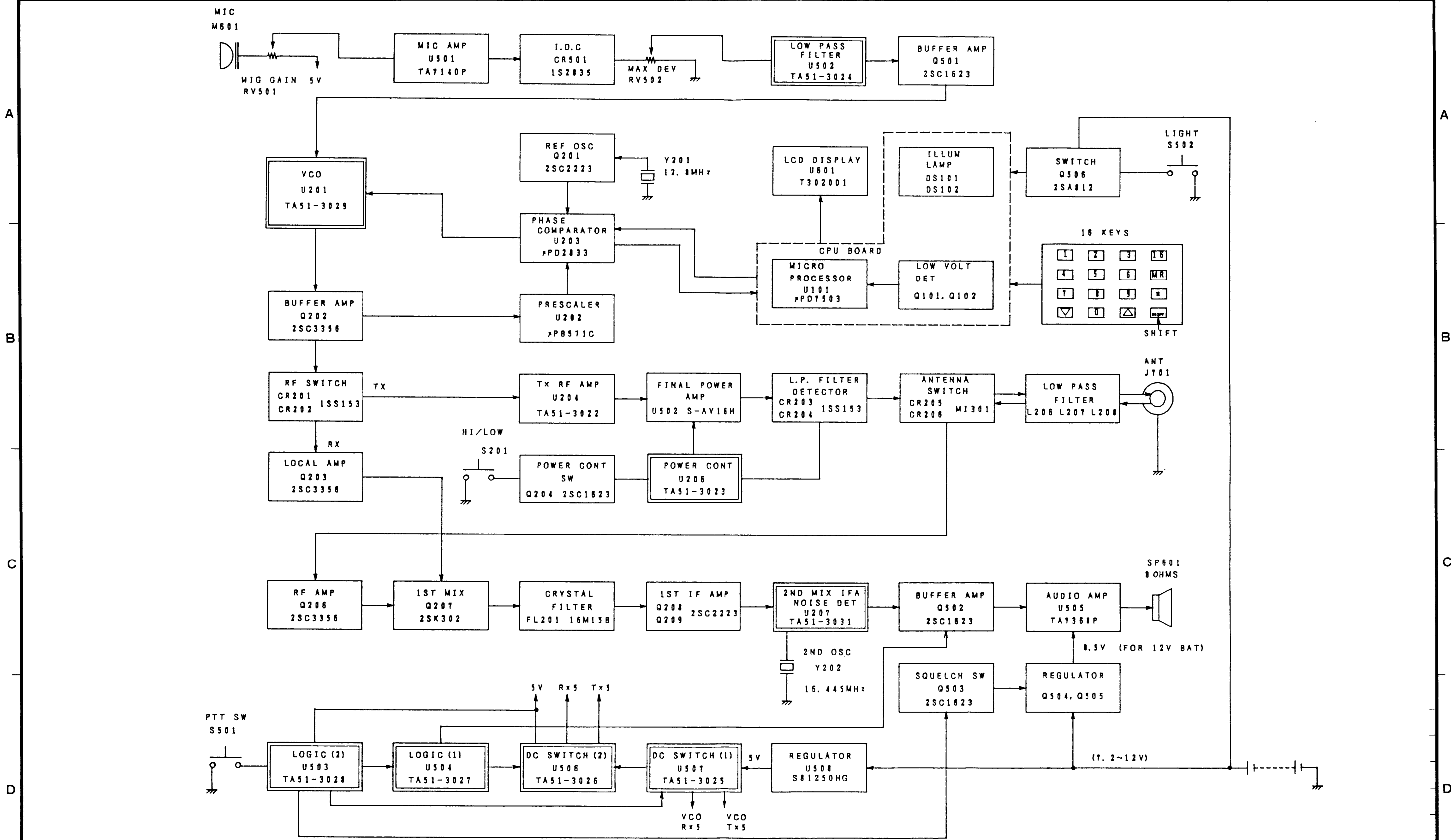
NOTE:  
備考:

SYMBOL 記号	CODE NO. コード番号	TYPE 型名	SYMBOL 記号	CODE NO. コード番号	TYPE 型名
BATTERY PACK (Dwg. No. C5460-008-A)					
DIODE					
CR951-1	5008056011	RD5.6JSB1			*1
CR951-2	5008075011	RD7.5JSB1			*2
CR951-3	5008091012	RD9.1JSB2			*3
CR952	5010010201	SR1K-2			
CR953	5011810401	ERB81-004			
JACK					
J 951	7033077901	HEC0779-01-030			
RESISTOR					
R 951-1	1005470301	RD1/4S47KnJ			*1
R 951-2	1005470301	RD1/4S47KnJ			*2
R 951-3	1005470301	RD1/4S47KnJ			*3
R 952-1	1008100121	RSF1B100nJ			*1
R 952-2	1008510021	RSF1B51nJ			*2
R 952-3	8019006001	*0.6			*3
SWITCH					
S 951	7009000101	RDE1B5A			

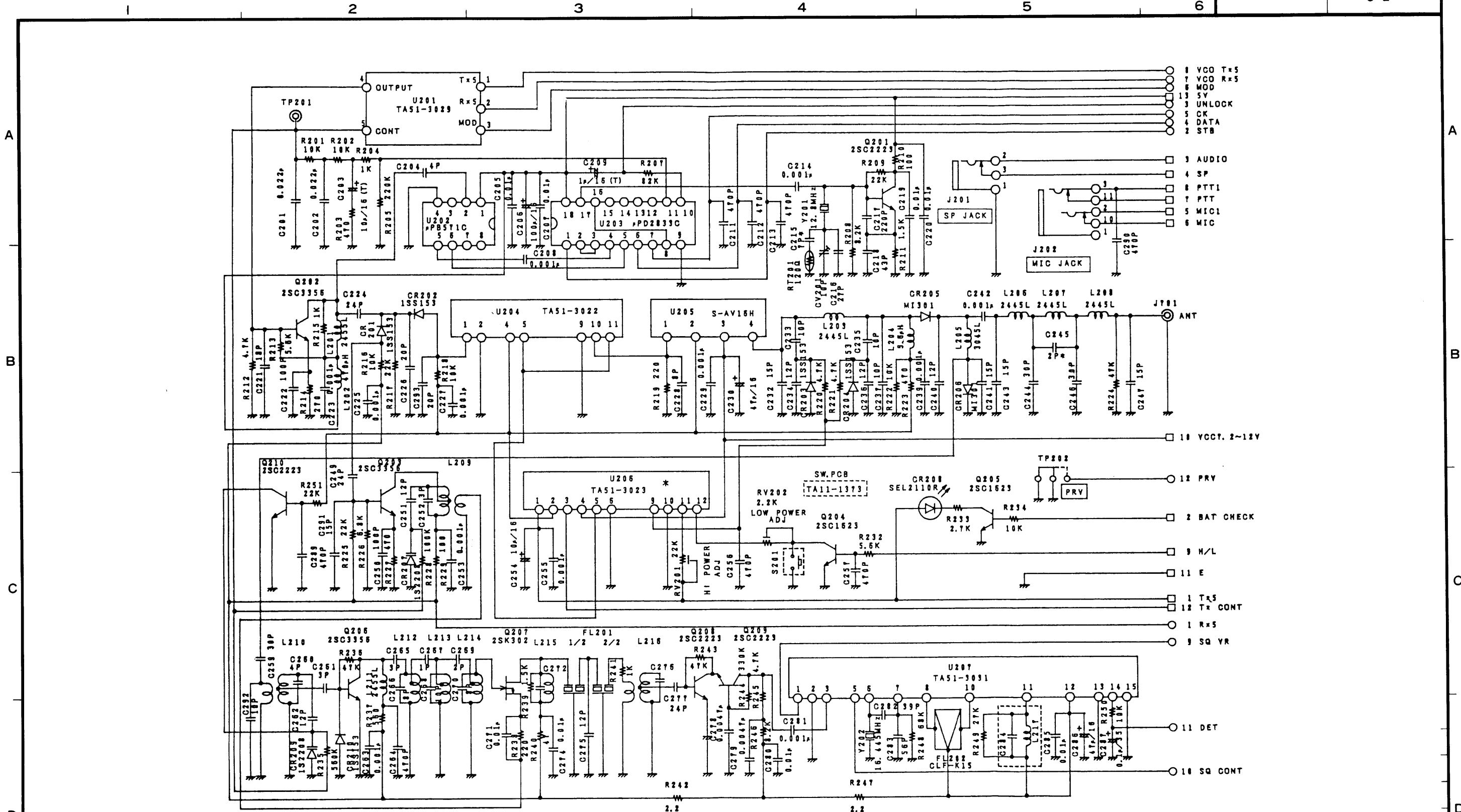
NOTE:

- 備考: \*1: For 7.2V  
\*2: For 9.6V  
\*3: For 12V

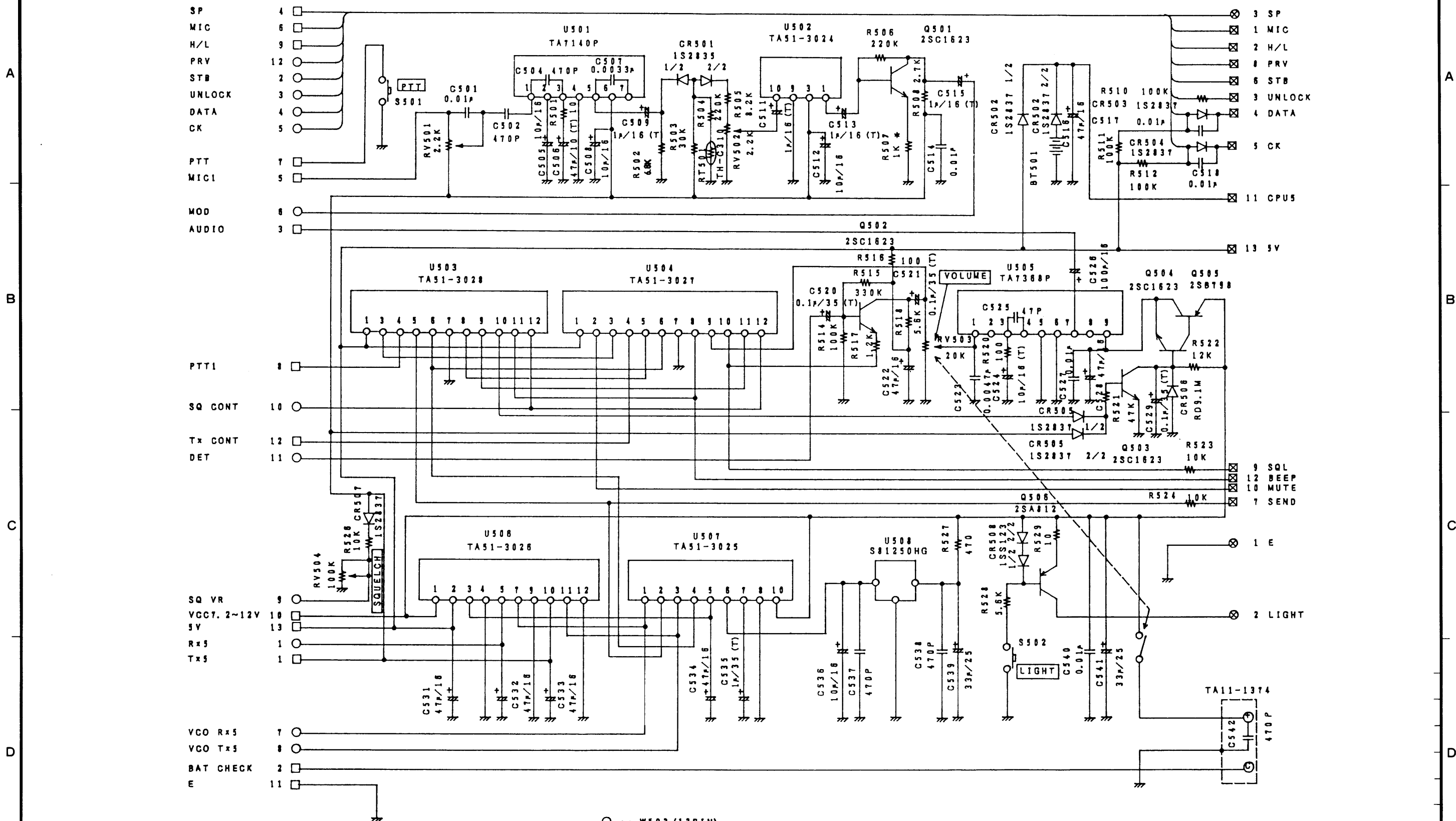




承認 APPROVED	JULY.22.'87 T. NAKANO	名称 TITLE	ブロック図 FM-55
検図 CHECKED	July 22 '87 M. IKAEDA		BLOCK DIAGRAM
製図 DRAWN	July 22 '87 H. MORI	図番 DWG. NO.	C5460-006-A

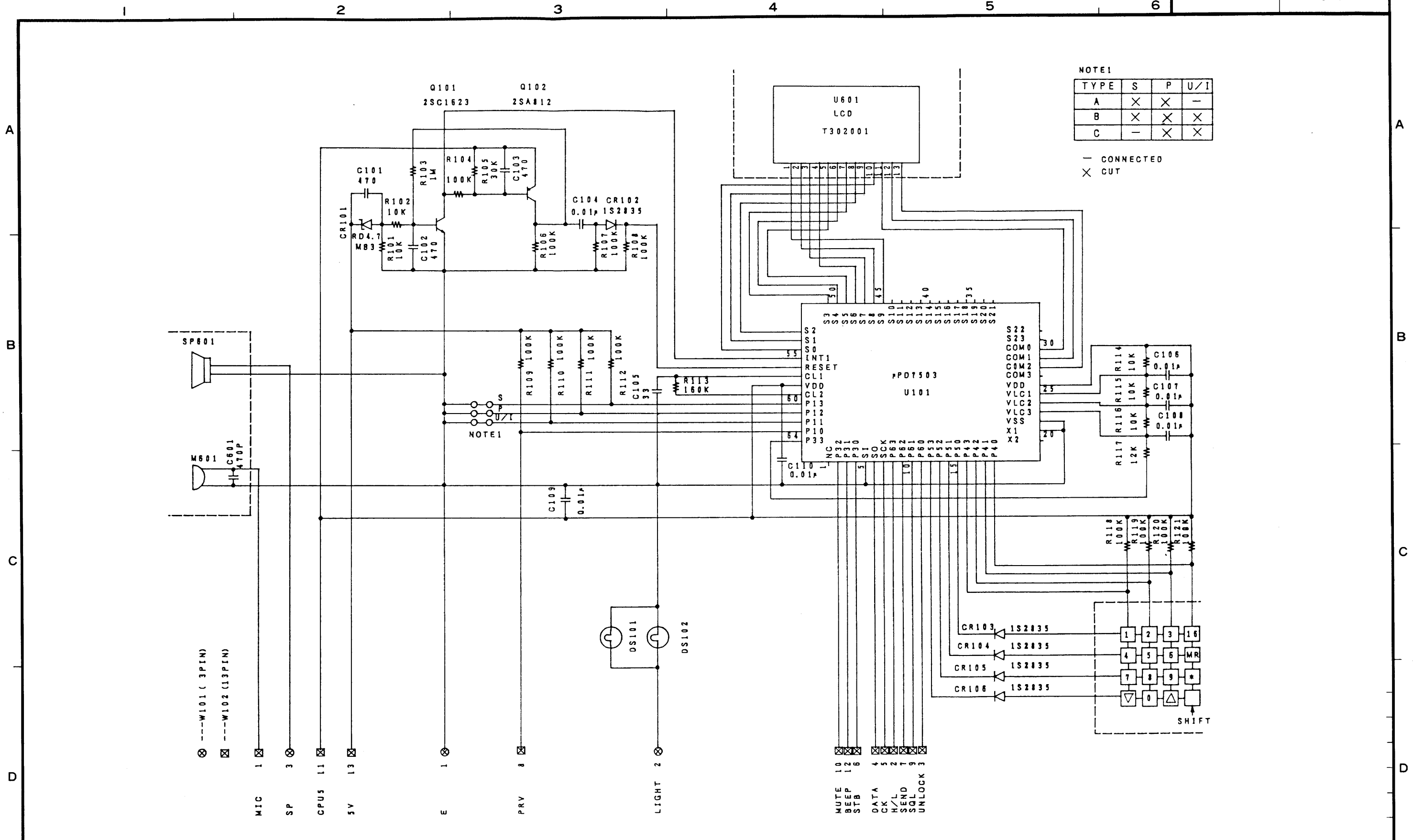


承認 APPROVED	JULY. 22. '87 T. NAKANO	名称 TITLE	RF 基板 FM-55 RF BOARD
検 CHECKED	July. 22. '87 M. IKEDA	図番 DWG. NO.	C5460-003-A
製 DRAWN	July. 22. '87 H. MORI		



- --- W503 (12PIN)
- --- W504 (13PIN)
- ⊗ --- J501 (3PIN)
- ⊠ --- J502 (13PIN)

承認 APPROVED	JULY. 22. '87 T. NAKANO	名称 TITLE	FM-55 A F 基板 AF BOARD
檢 CHECKED	July. 22. '87 M. IKEDA	製 DRAWN	製番 DWG. NO. C5460-005-A
製 DRAWN	July. 22. '87 H. MORI		



NOTE1

TYPE	S	P	U/I
A	X	X	-
B	X	X	X
C	-	X	X

- CONNECTED  
X CUT

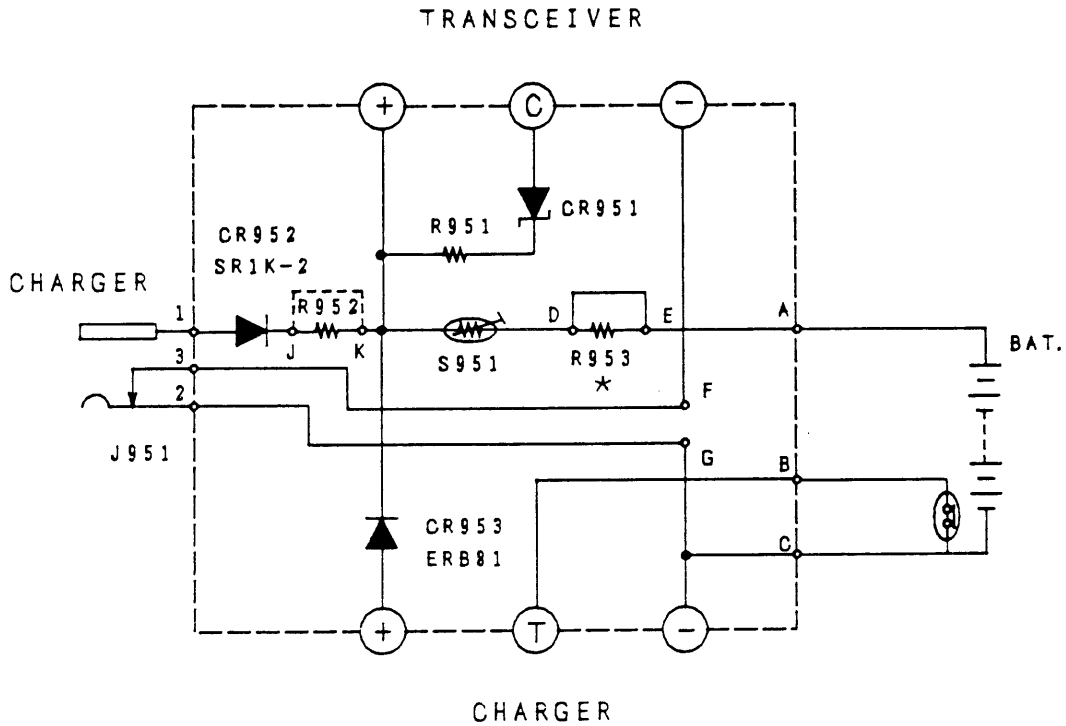
承認 APPROVED	JULY.22.'87 T. NAKANO	名称 TITLE	FM-55 CPU基板 CPU BOARD
検査 CHECKED	July.22.'87 M. IKEDA	製 DRAWN	製 DRAWN
製 DRAWN	July.22.'87 H. MORI	図番 DWG.NO.	C5460-004-A

A

B

C

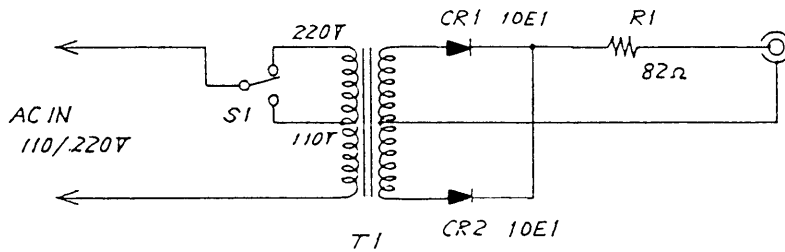
D



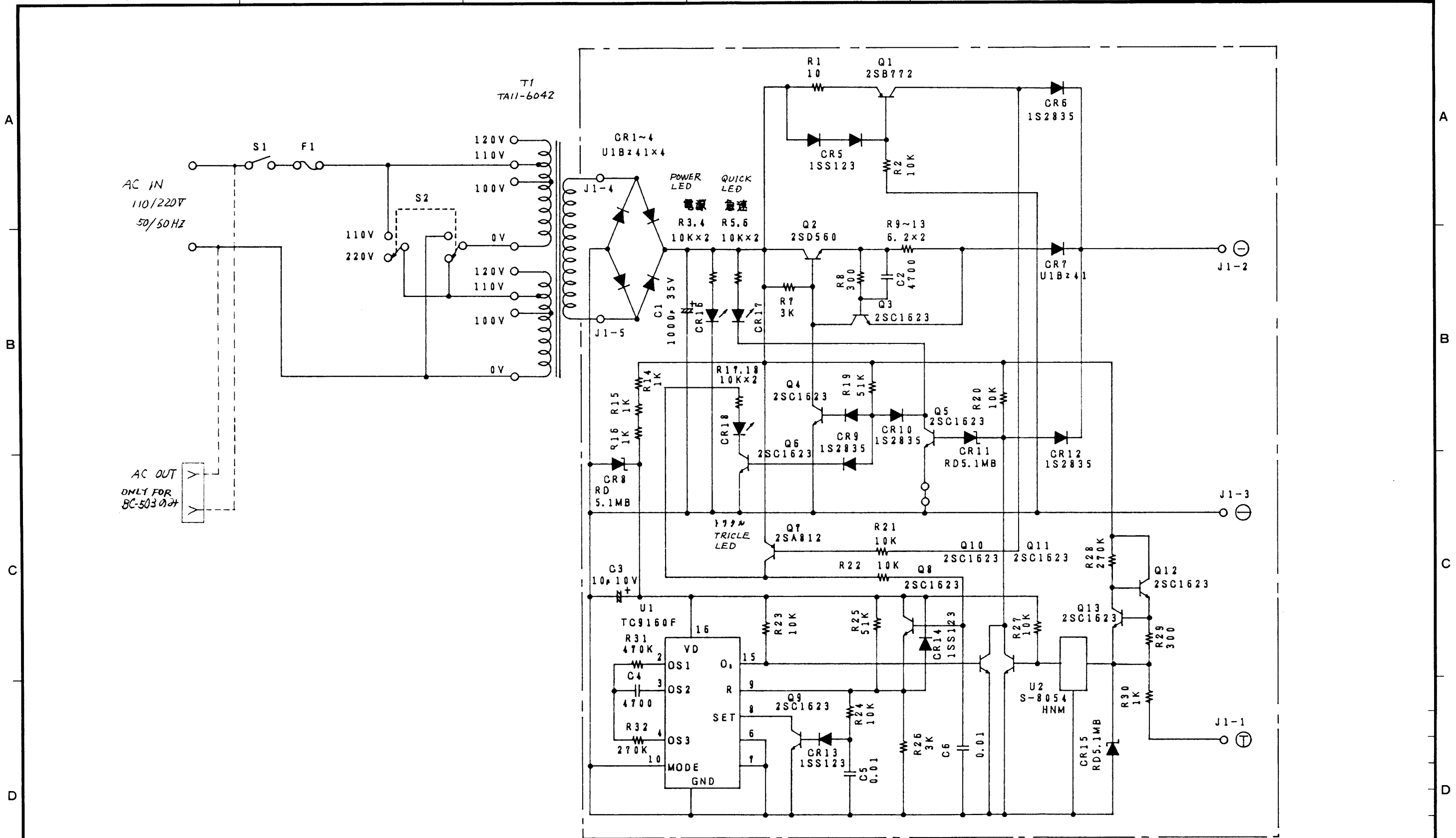
TYPE	R951	R952	CR951
BP-507 (7.2V 450mAH)	47K	100 1W	RD5.6JSB1
BP-807 (7.2V 800mAH)	47K	100 1W	RD5.6JSB1
BP-509 (9.6V 450mAH)	47K	51 1W	RD7.5JSB1
BP-512 (12V 450mAH)	47K	Shorted	RD9.1JSB2

\* Not mounted in standard set

承認 APPROVED	品番 ITEM	品名 NAME	材質 MATERIAL	数量 Q'TY	図番 DWG. NO.	摘要 REMARKS
JULY.22.87 T. NAKANO	FM-55	三角法 THIRD ANGLE PROJECTION				名称 TITLE バッテリーパック BATTERY PACK
検査 CHECKED	July.22.87 M. IKEDA	尺度 SCALE				
製図 DRAWN	July.22.87 H. MORI	重量 WEIGHT	kg		図番 DWG. NO. C5460-008-A	



FM-55		品番 ITEM	品名 NAME	材質 MATERIAL	数量 Q'TY	図番 DWG.NO.	摘要 REMARKS
承認 APPROVED	JULY. 22. '87 T. NAKANO	三角法 THIRD ANGLE PROJECTION		名称 TITLE BC-500 ウォールチャージャー WALL CHARGER			
検図 CHECKED	July. 22. '87 M. IKEDA	尺度 SCALE	/				
製図 DRAWN	July. 22. '87 H. MORI	重量 WEIGHT	kg	図番 DWG.NO. C5460-007-A			



BC-503は [ ] で囲まれた回路を3系統内蔵している。  
 The BC-503 is composed of three circuits, one of which is encircled with [ ] .

FM-55/UF-6

承認 APPROVED	Aug. 7 '87 T. NAKAUO	名称 TITLE	一連 / 三連急速充電器 BC-501/503
検 CHECKED	Aug. 7 '87 M. IKEDA		TABLETOP QUICK CHARGER
製 DRAWN	Aug. 7 '87 H. MORI	図番 DWG. NO.	C5460-009-A